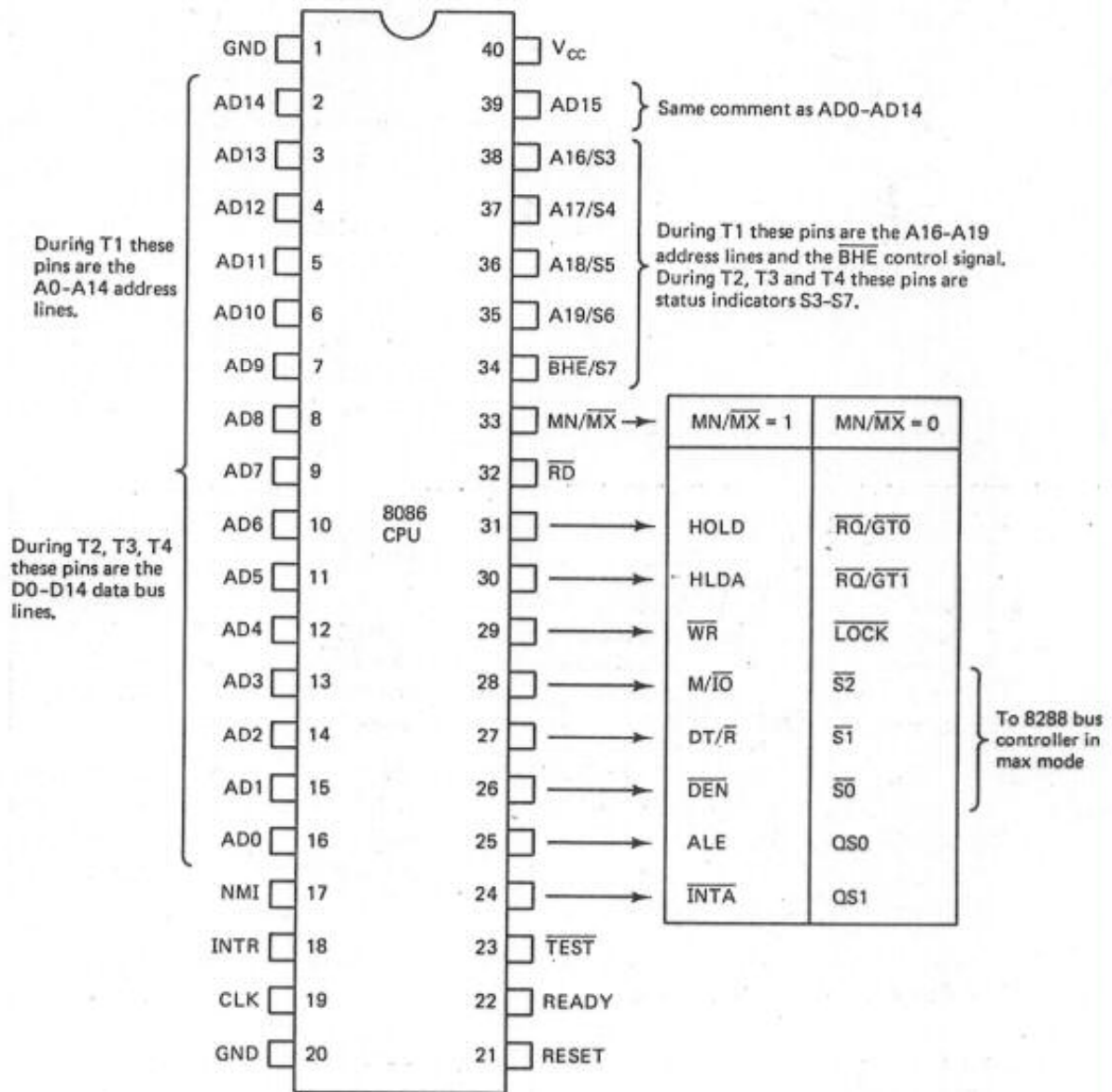
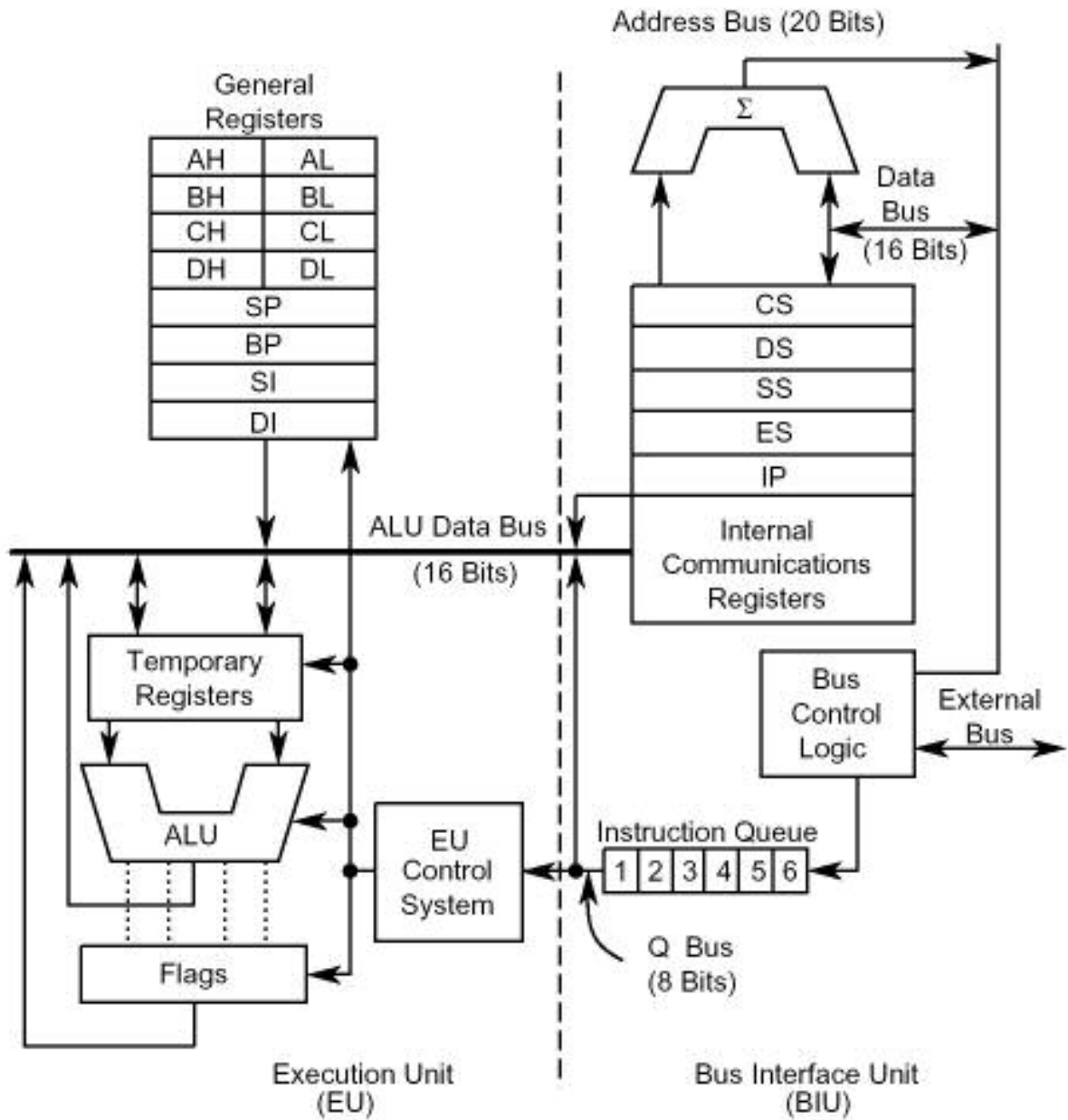


### 3. 80x86 Mikroişlemci Donanımı

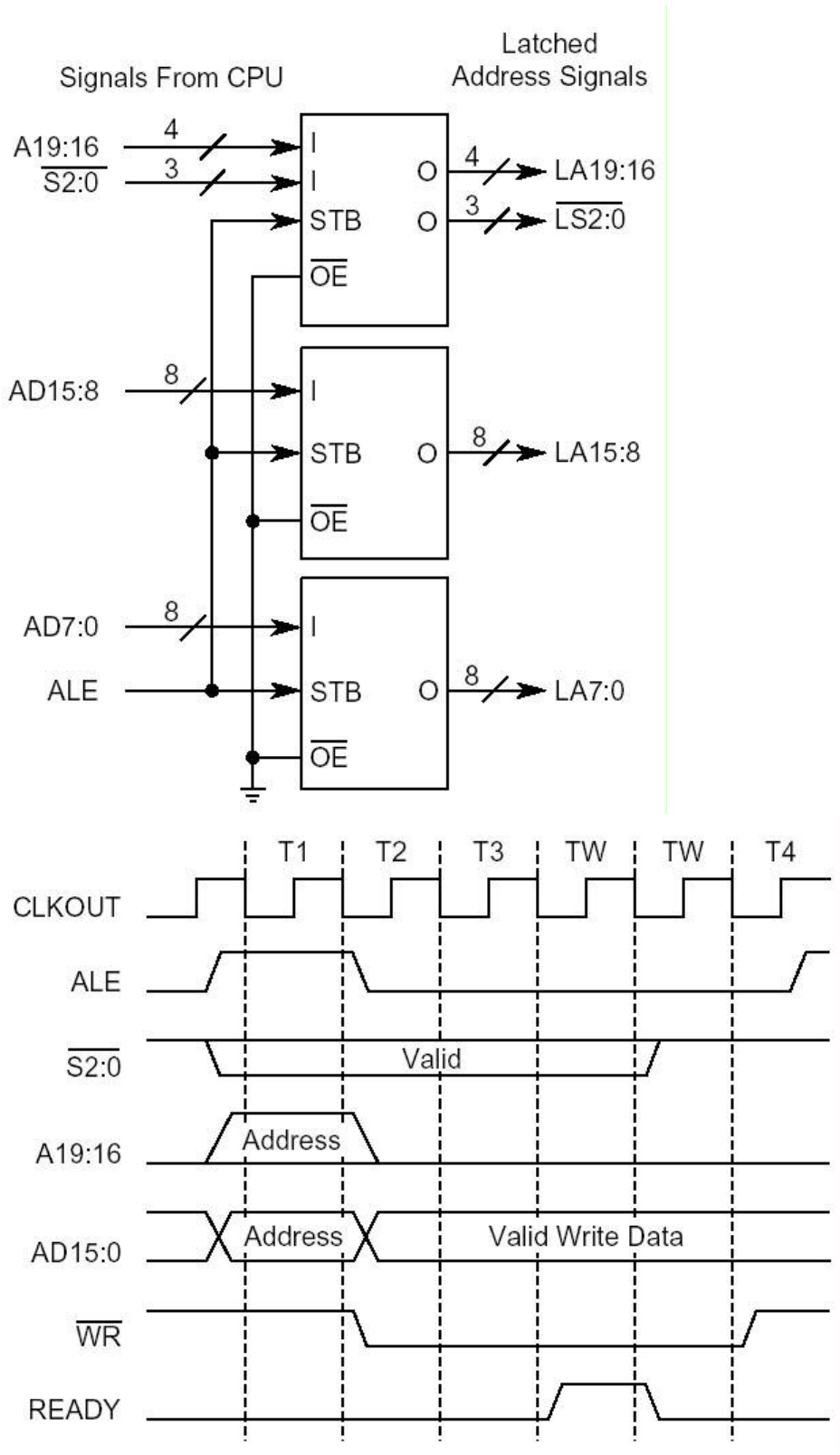
#### 3.1. 8086 Uç ayrıntıları



## 3.1.1. 8086 İç Blok Diyagramı

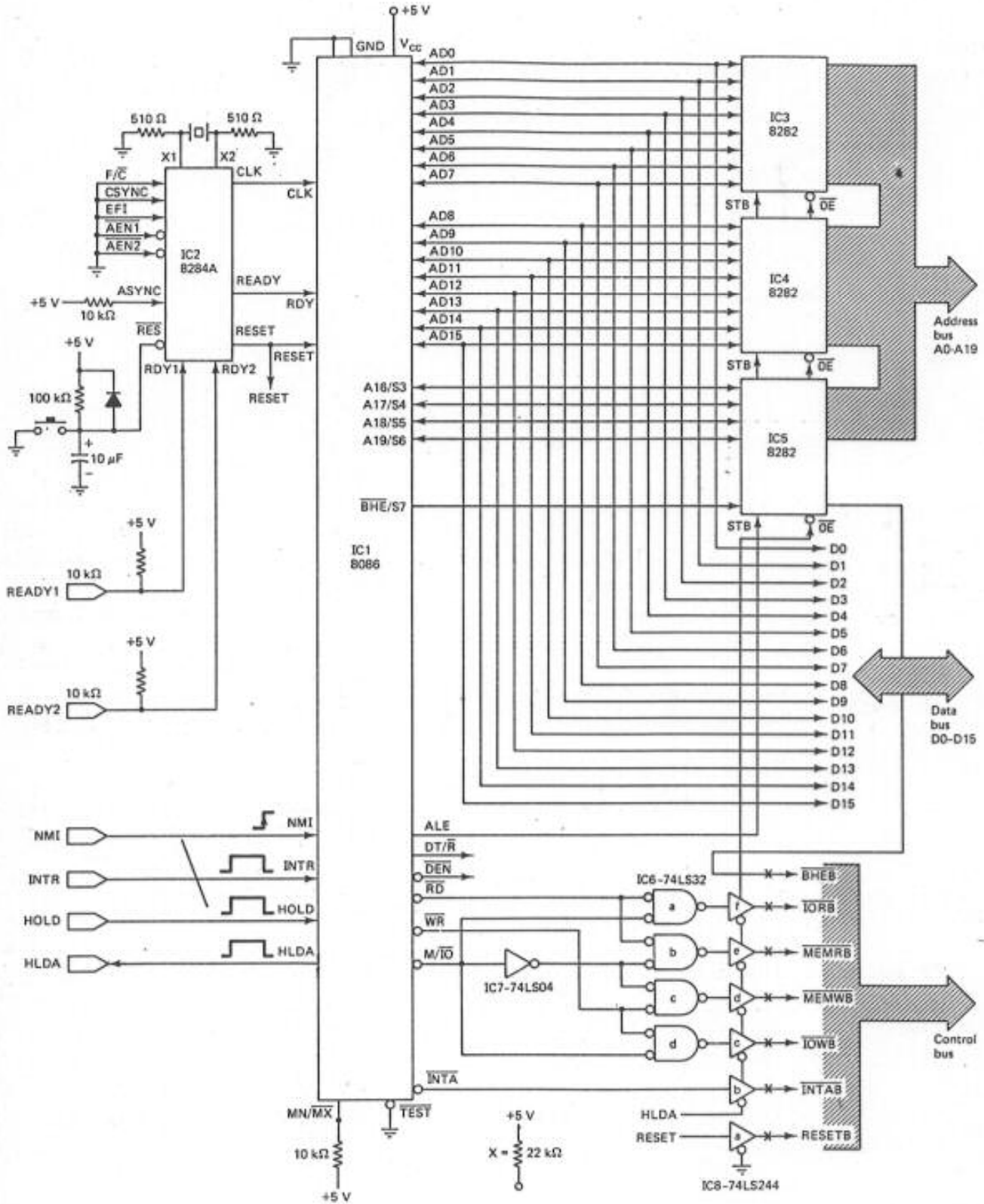


### 3.1.2. 8086 Adres/Veri Yolunun Ayrılması

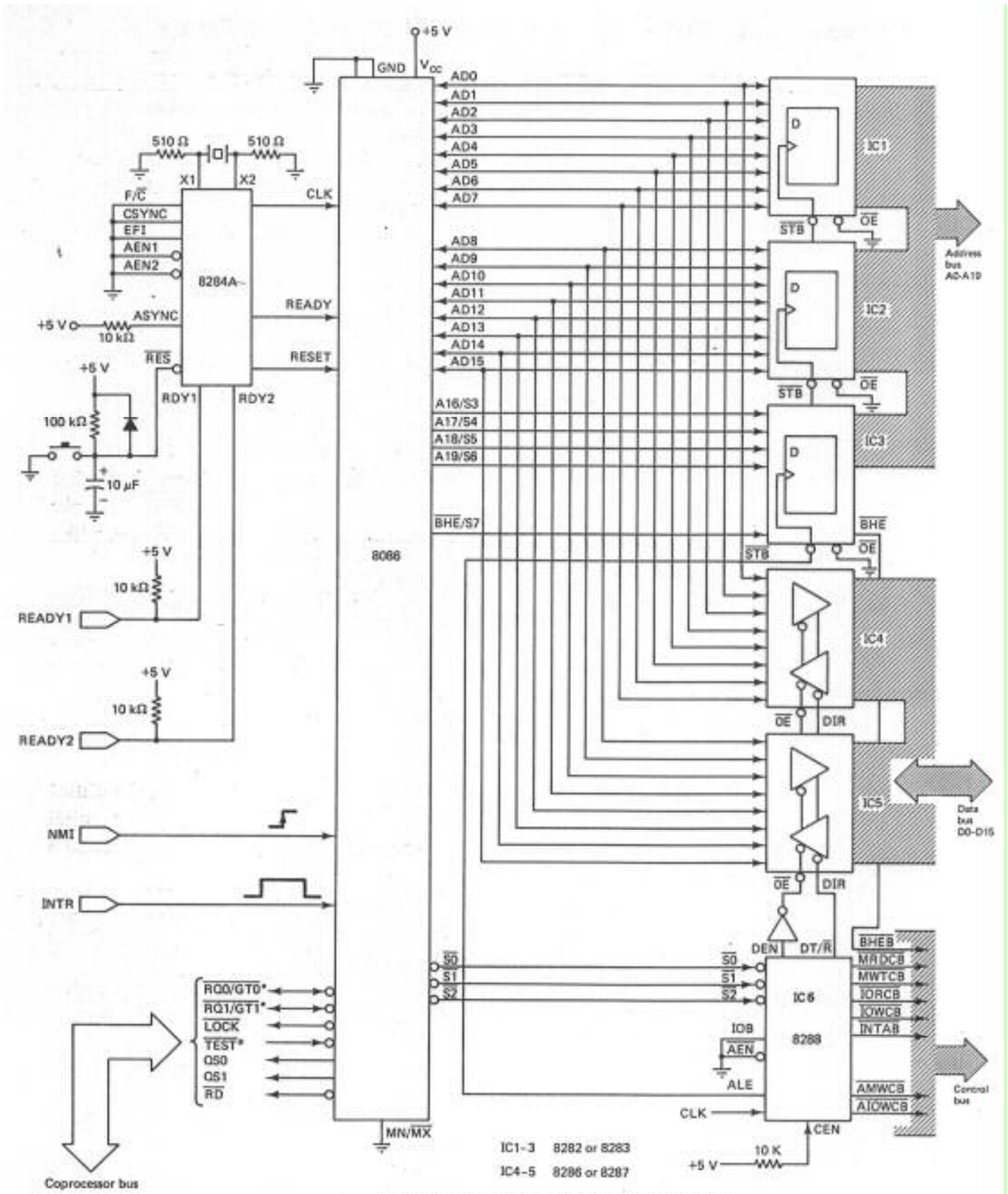


## 3.2. 8086 Mikroişlemci Çalışma Şekilleri

### 3.2.1. 8086 Mikroişlemcisinin Minimum Çalışma Şekli



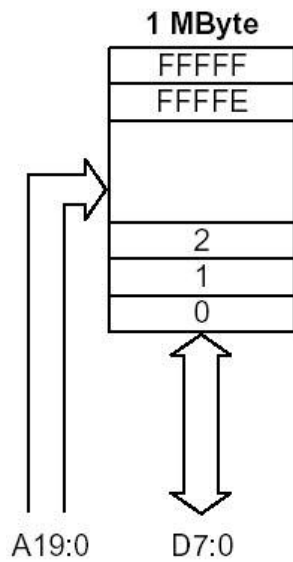
### 3.2.2. 8086 Mikroişlemcisinin Maksimum Çalışma Şekli



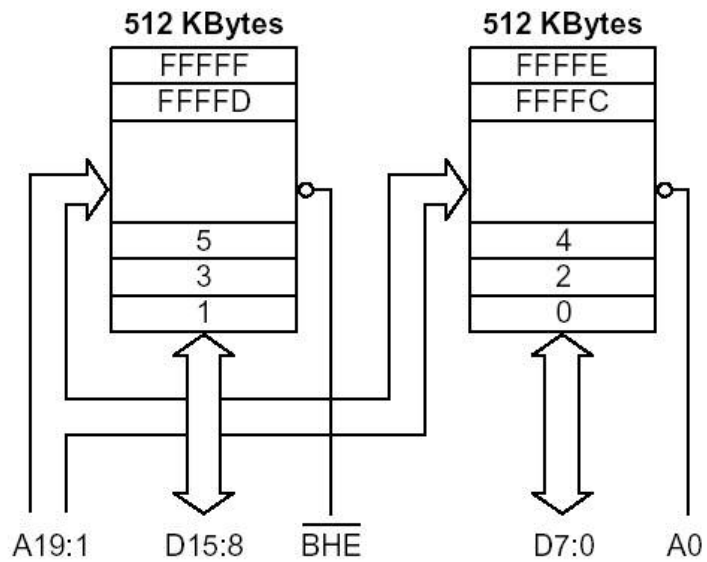
\*Connect these inputs to +5 V through 1 kΩ resistors if unused  
 \*Connect this pin to ground if unused

### 3.3. 8086 Fiziksel Bellek Yapısı

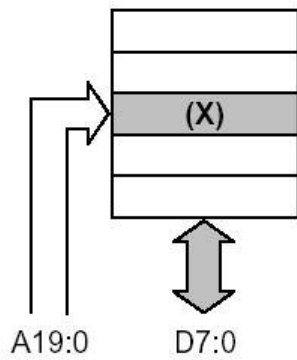
Physical Implementation  
of the Address Space for  
8-Bit Systems



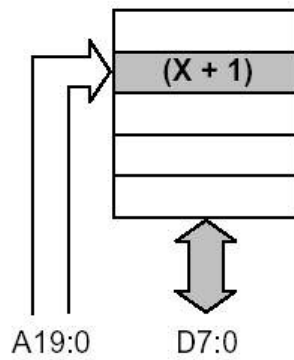
Physical Implementation  
of the Address Space for  
16-Bit Systems

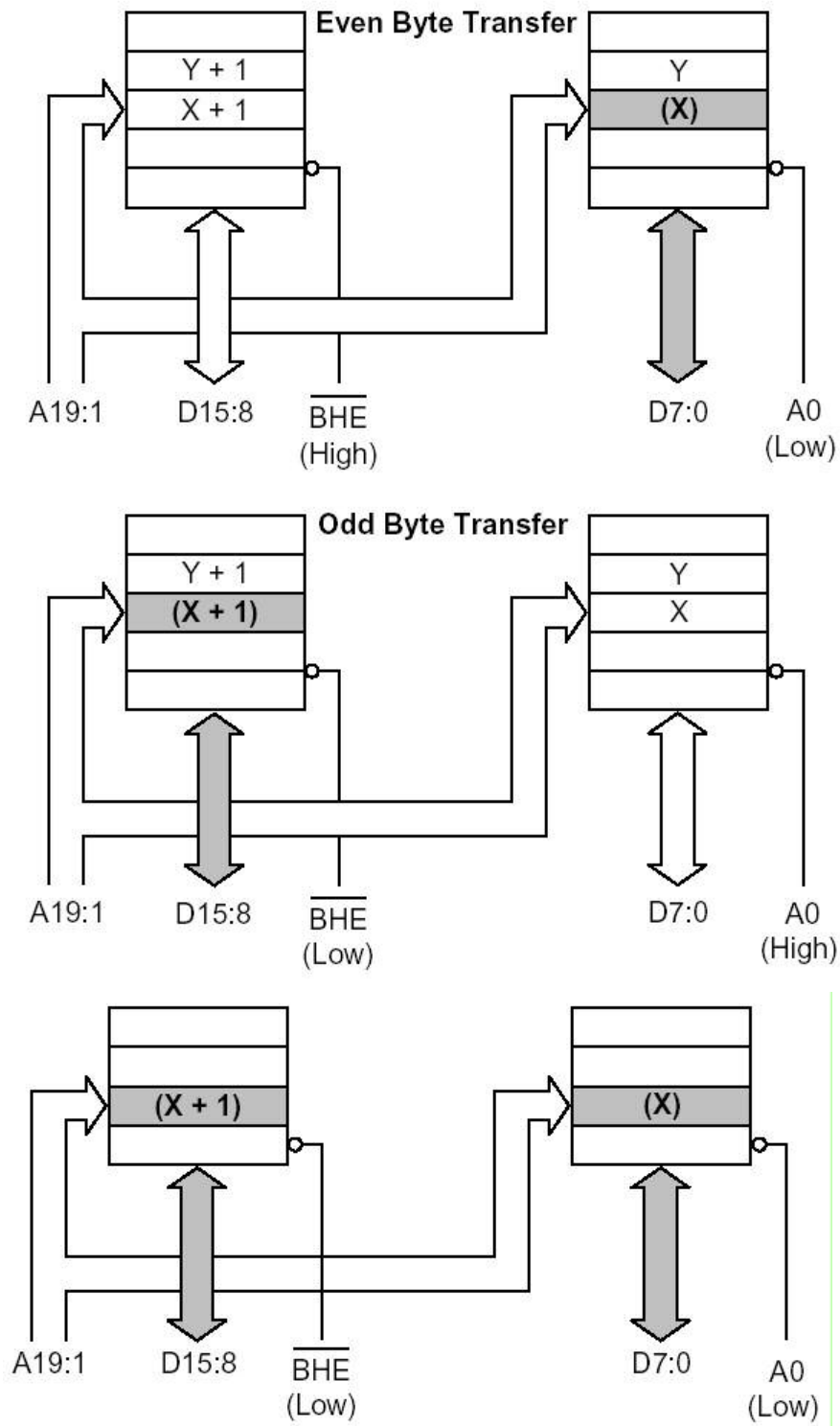


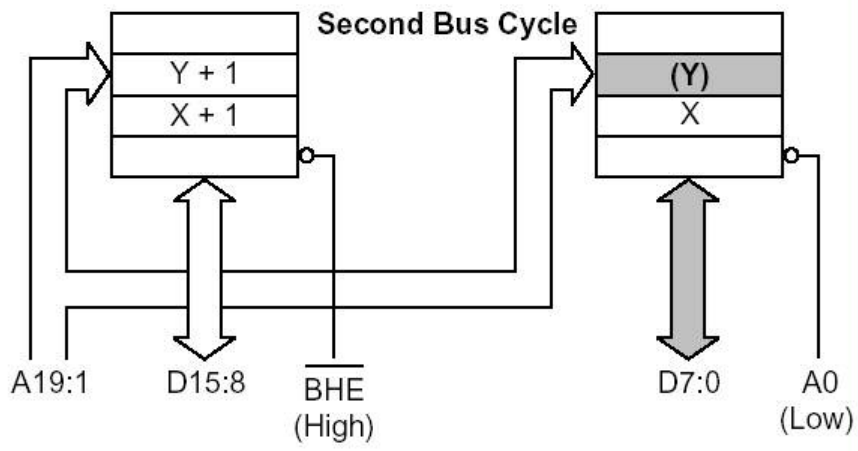
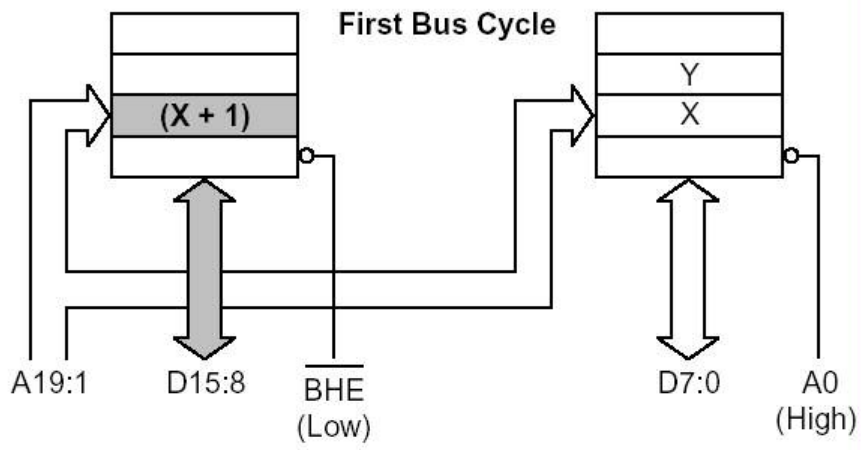
**First Bus Cycle**



**Second Bus Cycle**

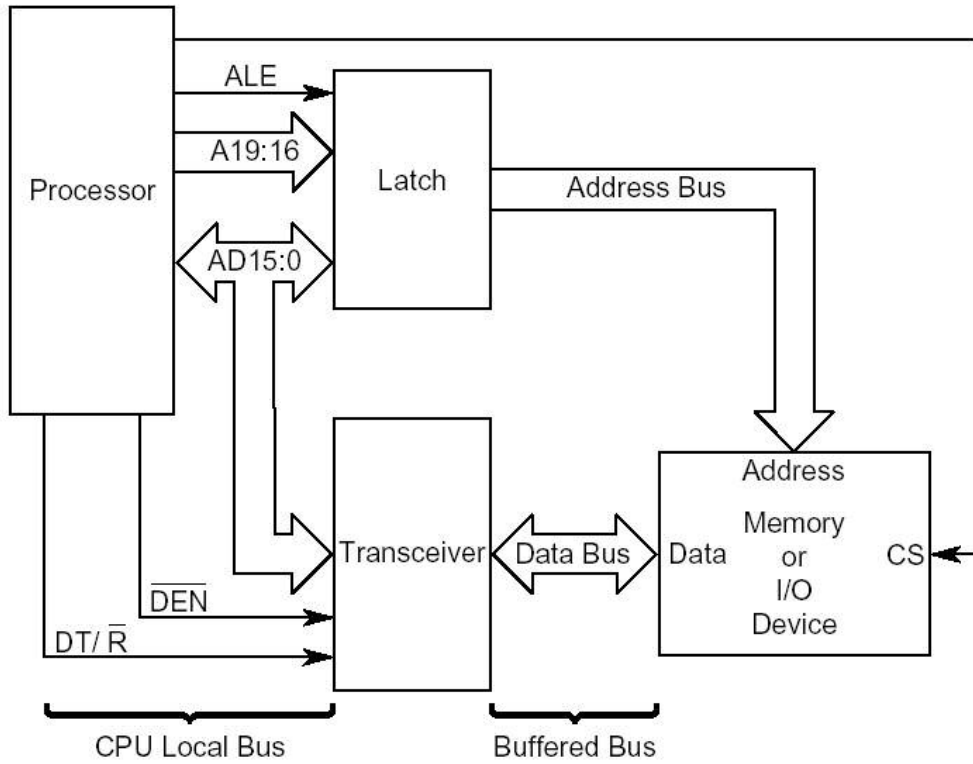




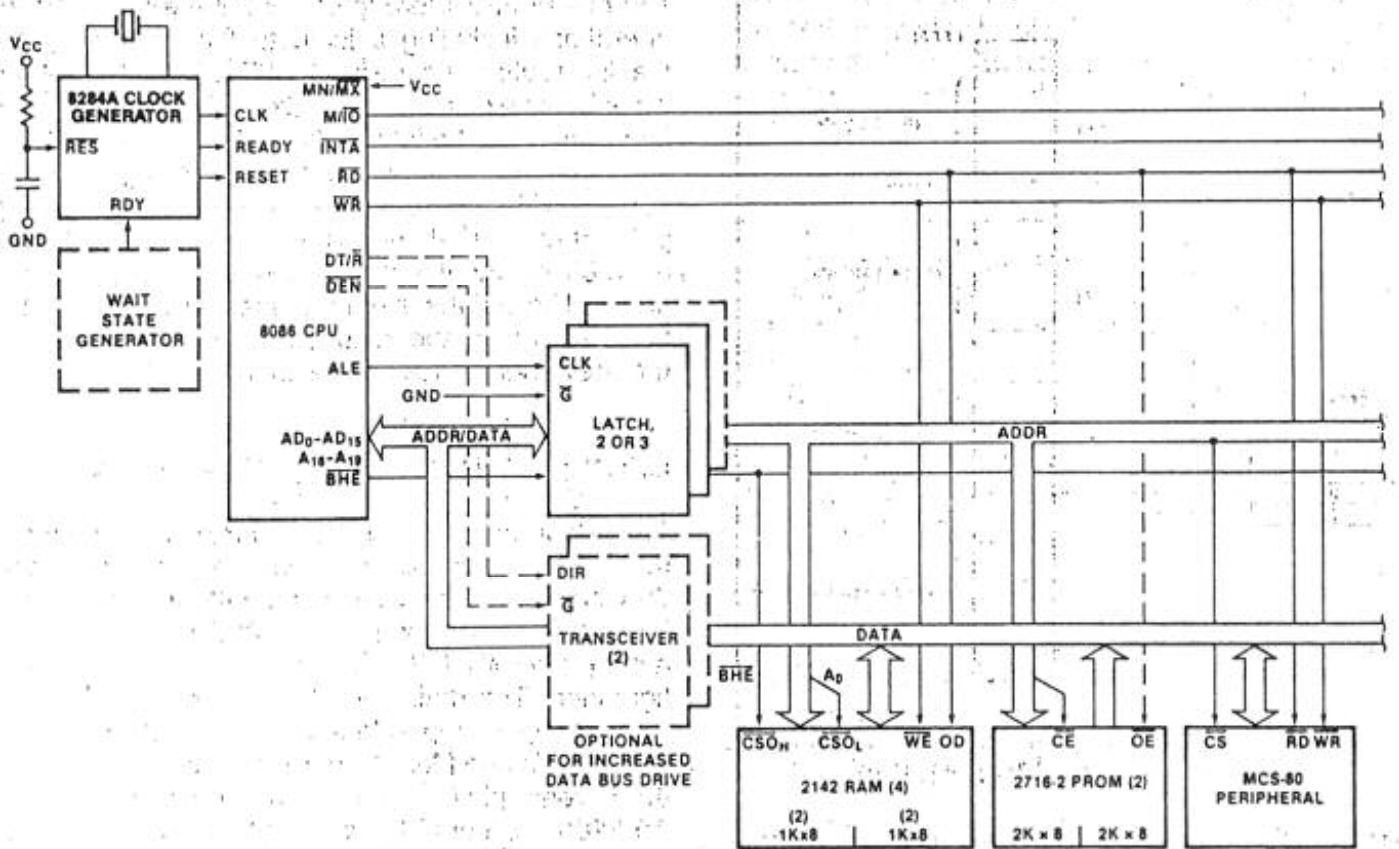




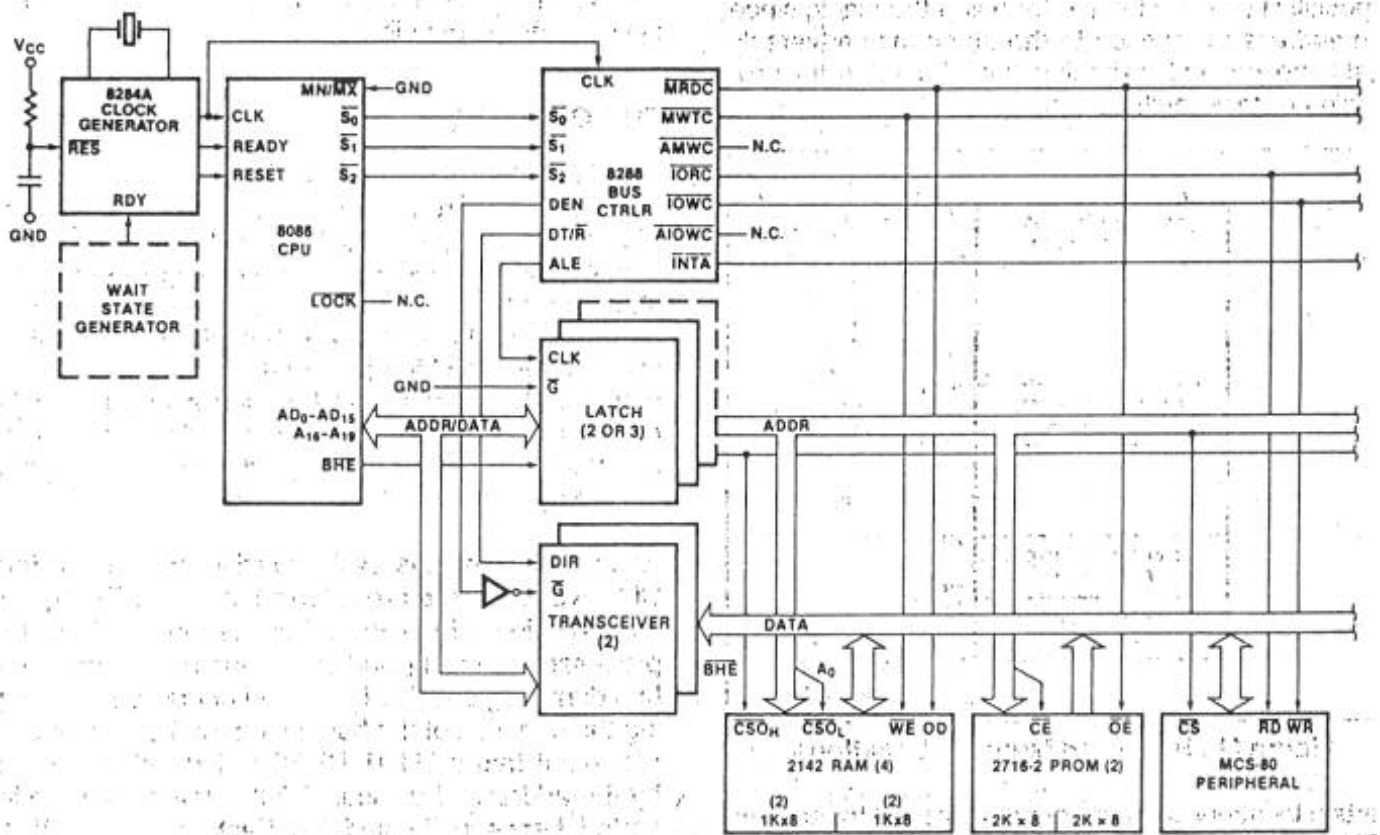
### 3.4. 8086 Mikroişlemci Temelli Sistem Yapıları



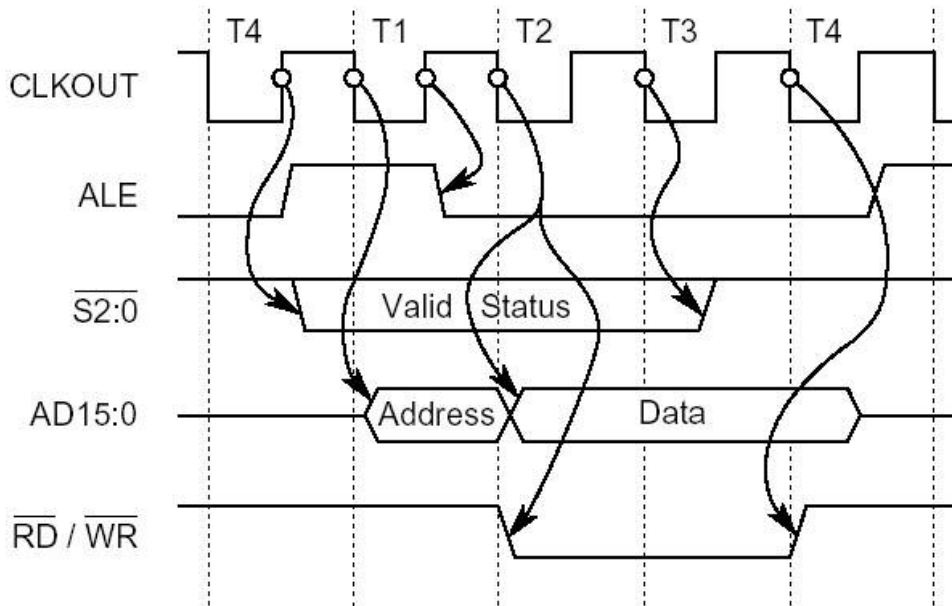
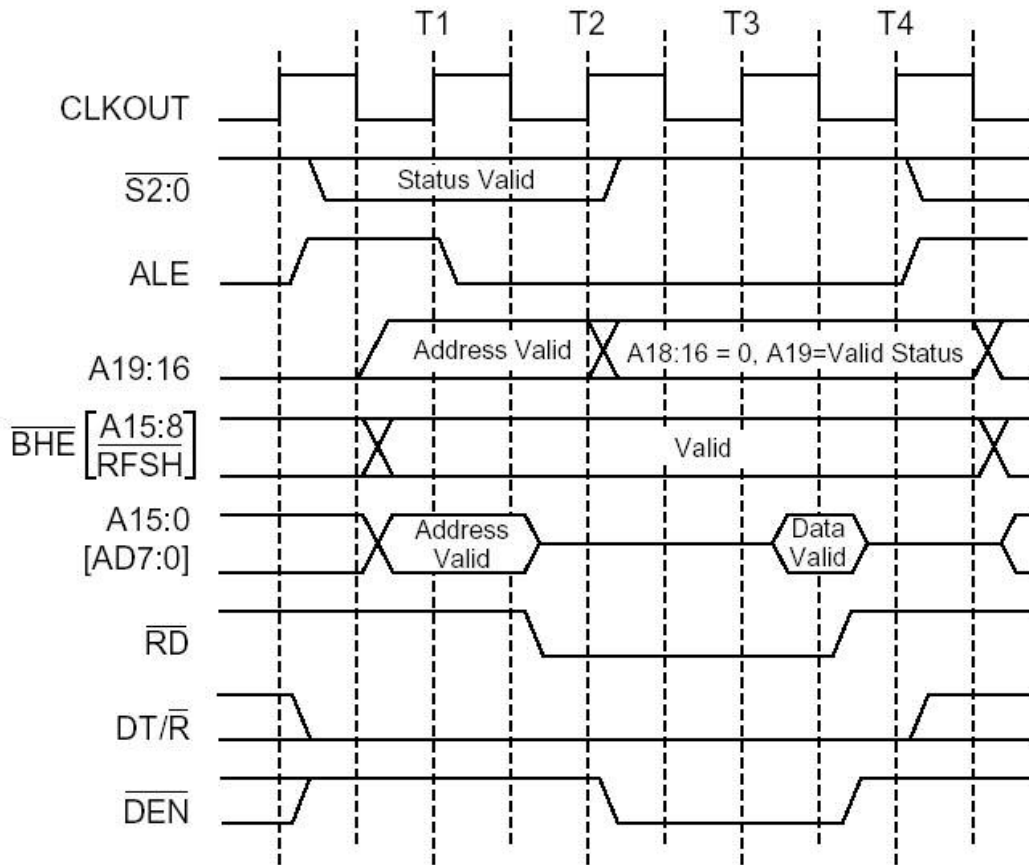
#### 3.4.1. 8086 Mikroişlemci Temelli Minimum Sistem Yapısı

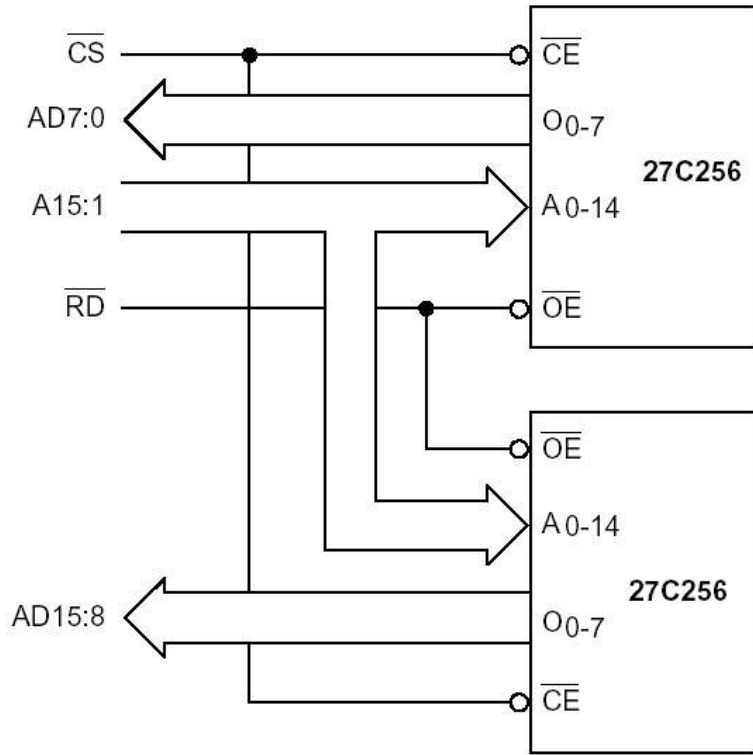


### 3.4.2. 8086 Mikroişlemci Temelli Maksimum Sistem Yapısı

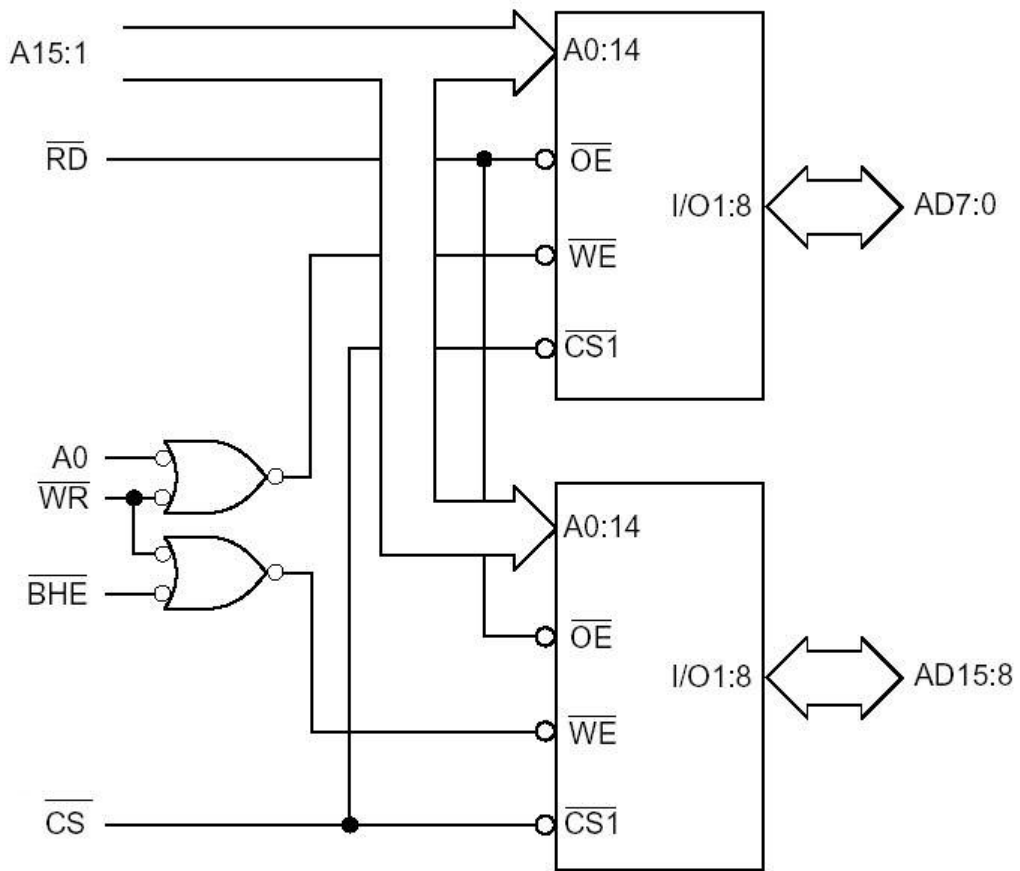


### 3.5. Ana Bellek Birimi



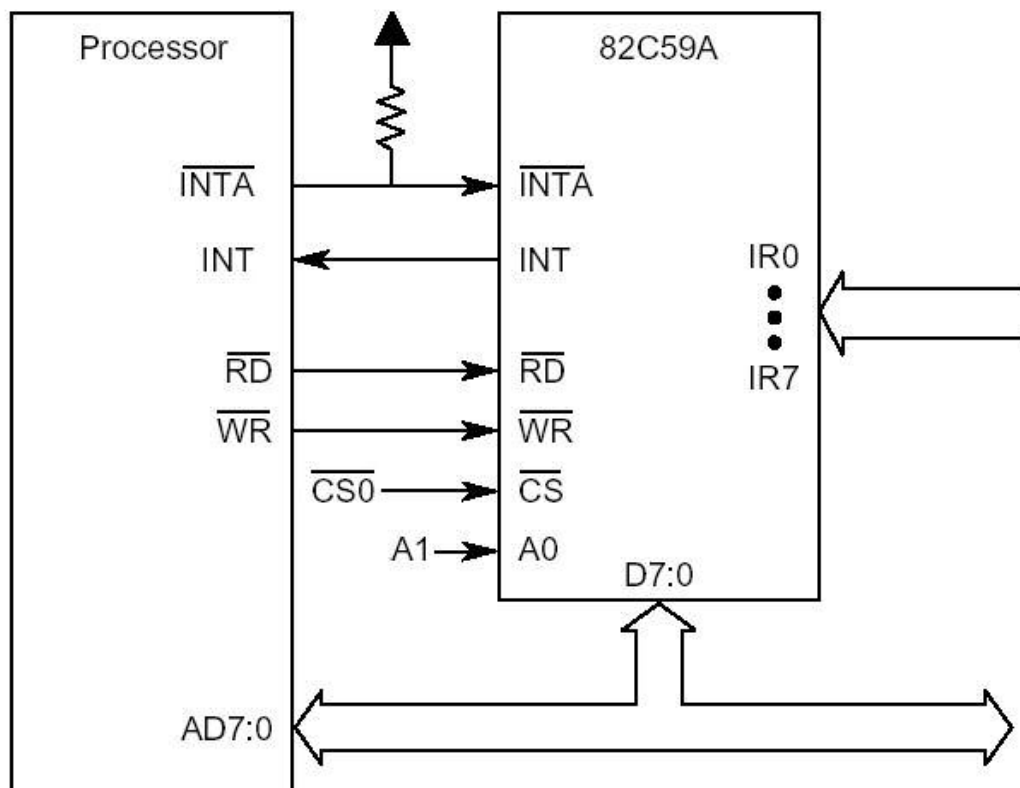
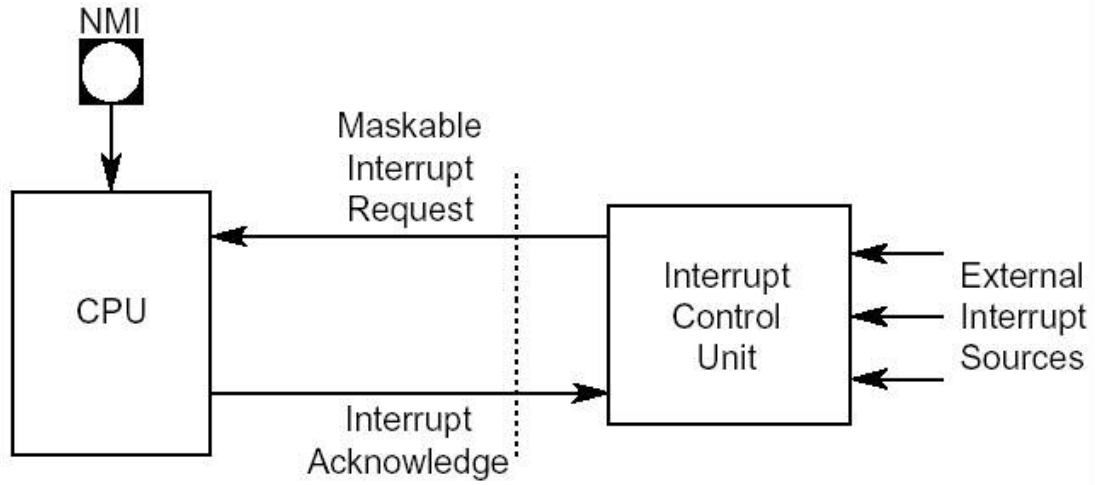


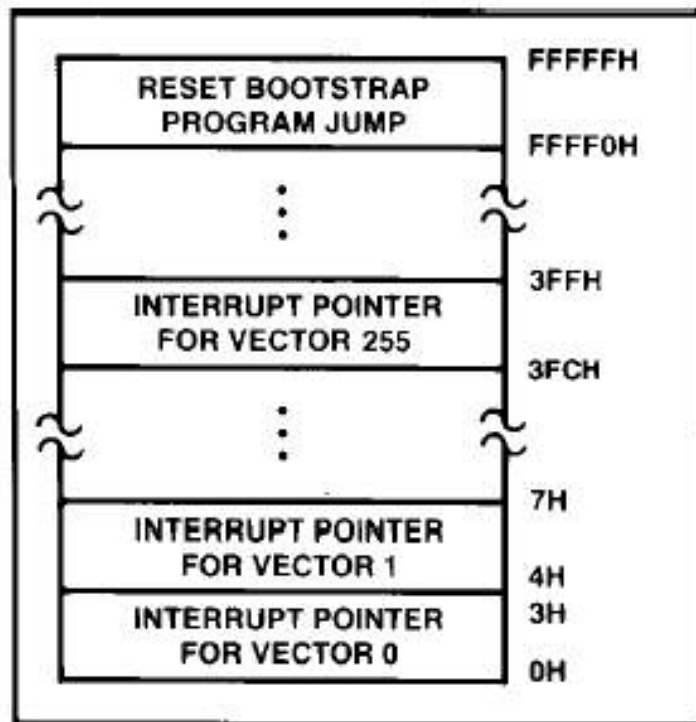
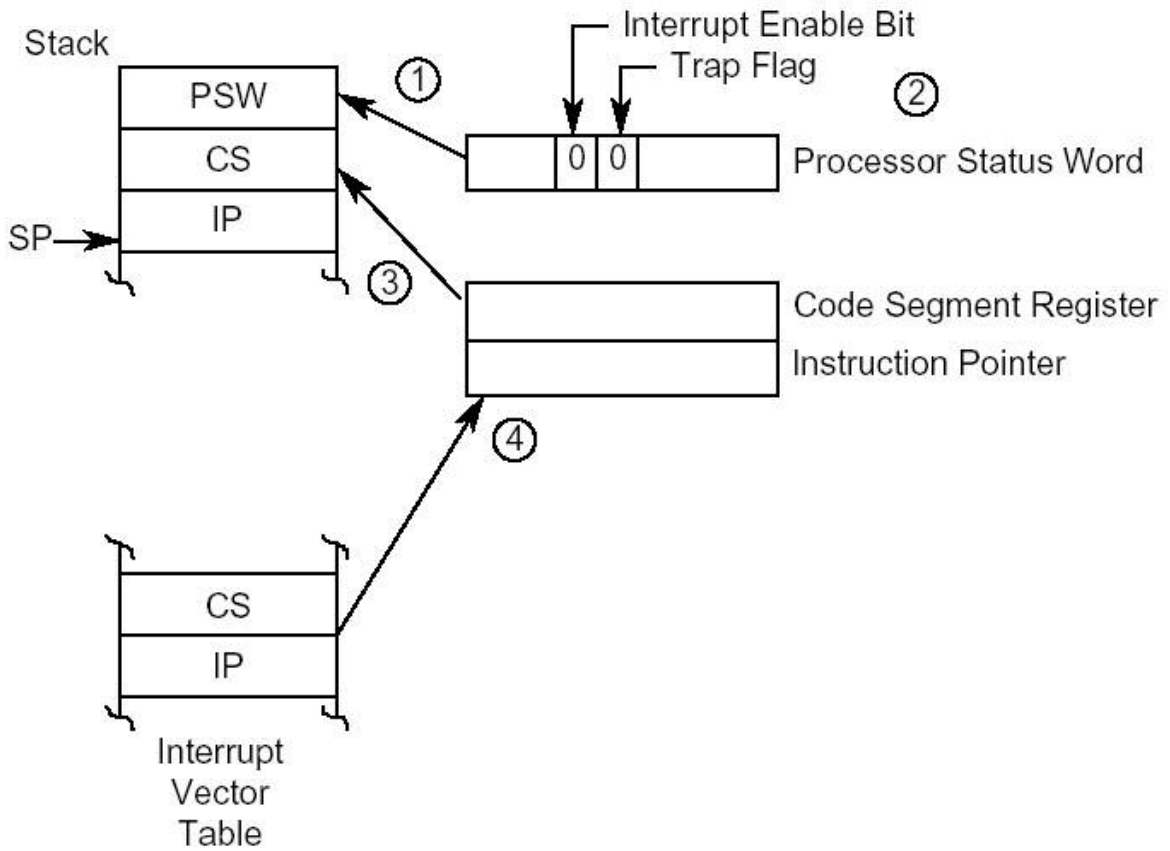
**Note:**  $A_0$  and  $\overline{BHE}$  are not used.



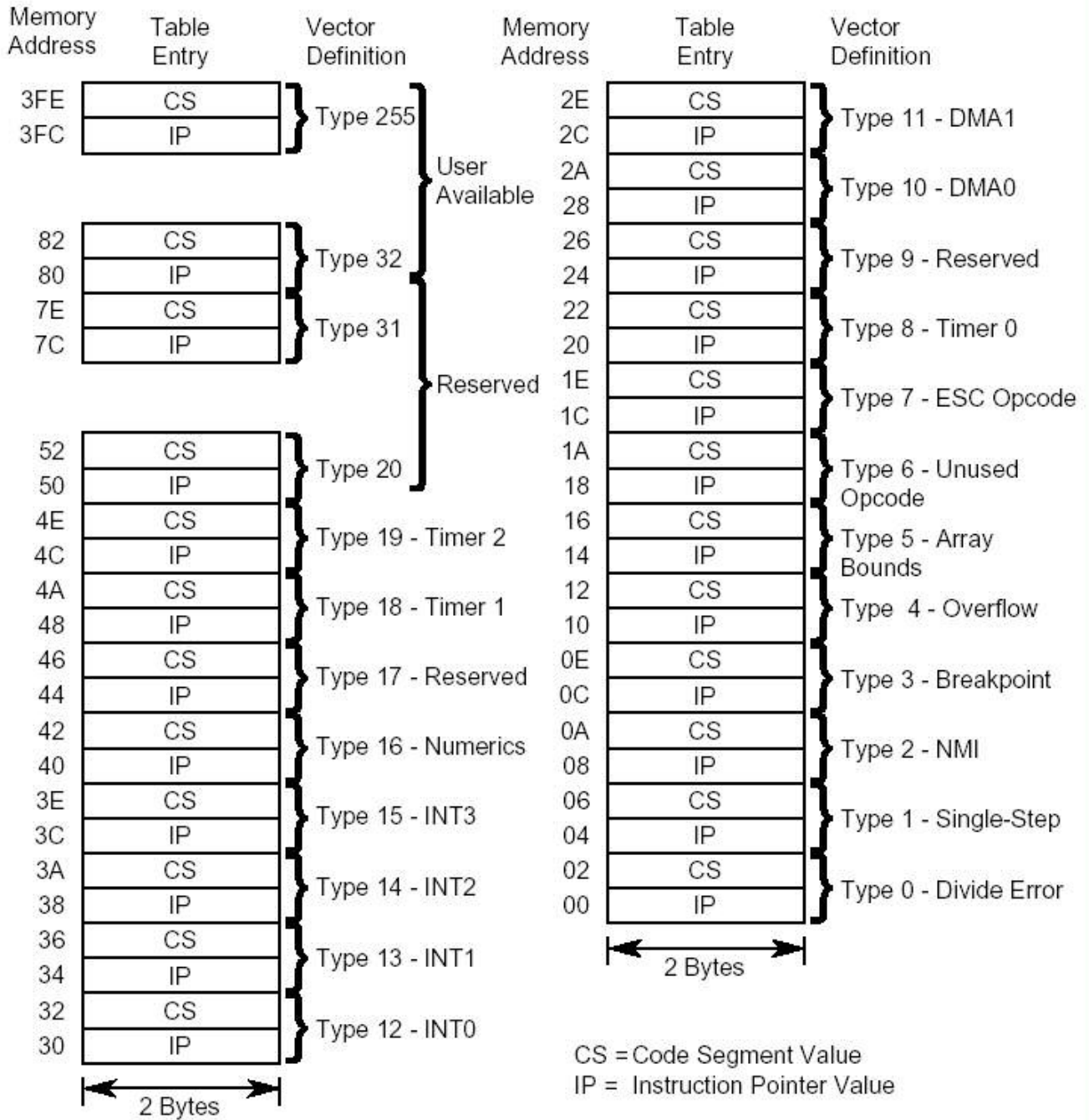
RAM Bağlantısı

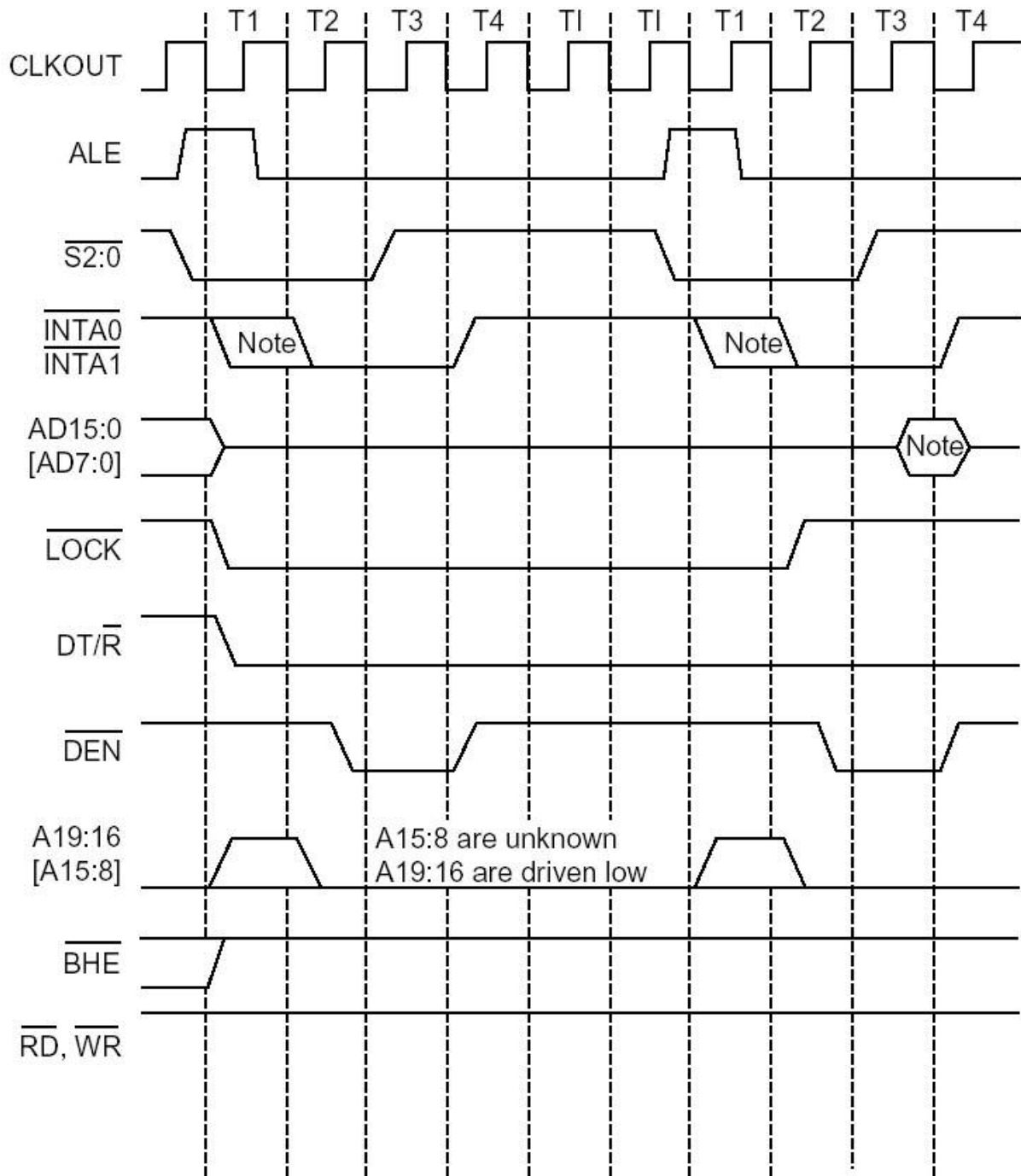
### 3.6. 8086 Kesme Yapısı





INITIAL CS:IP VALUE IS F000:FFF0.





**NOTE:** Vector Type is read from AD7:0 only.  
INTA occurs during T2 in slave mode.