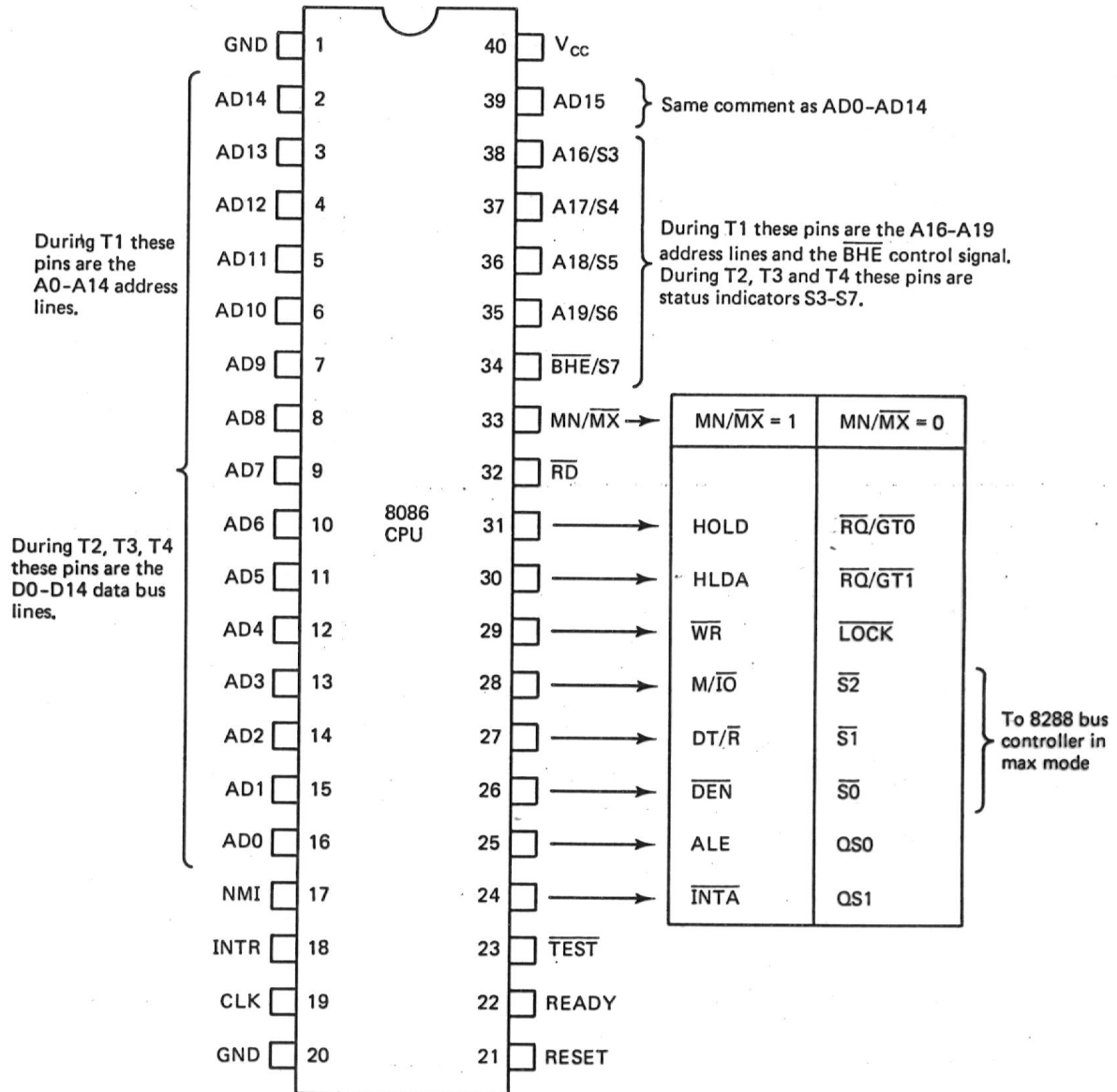
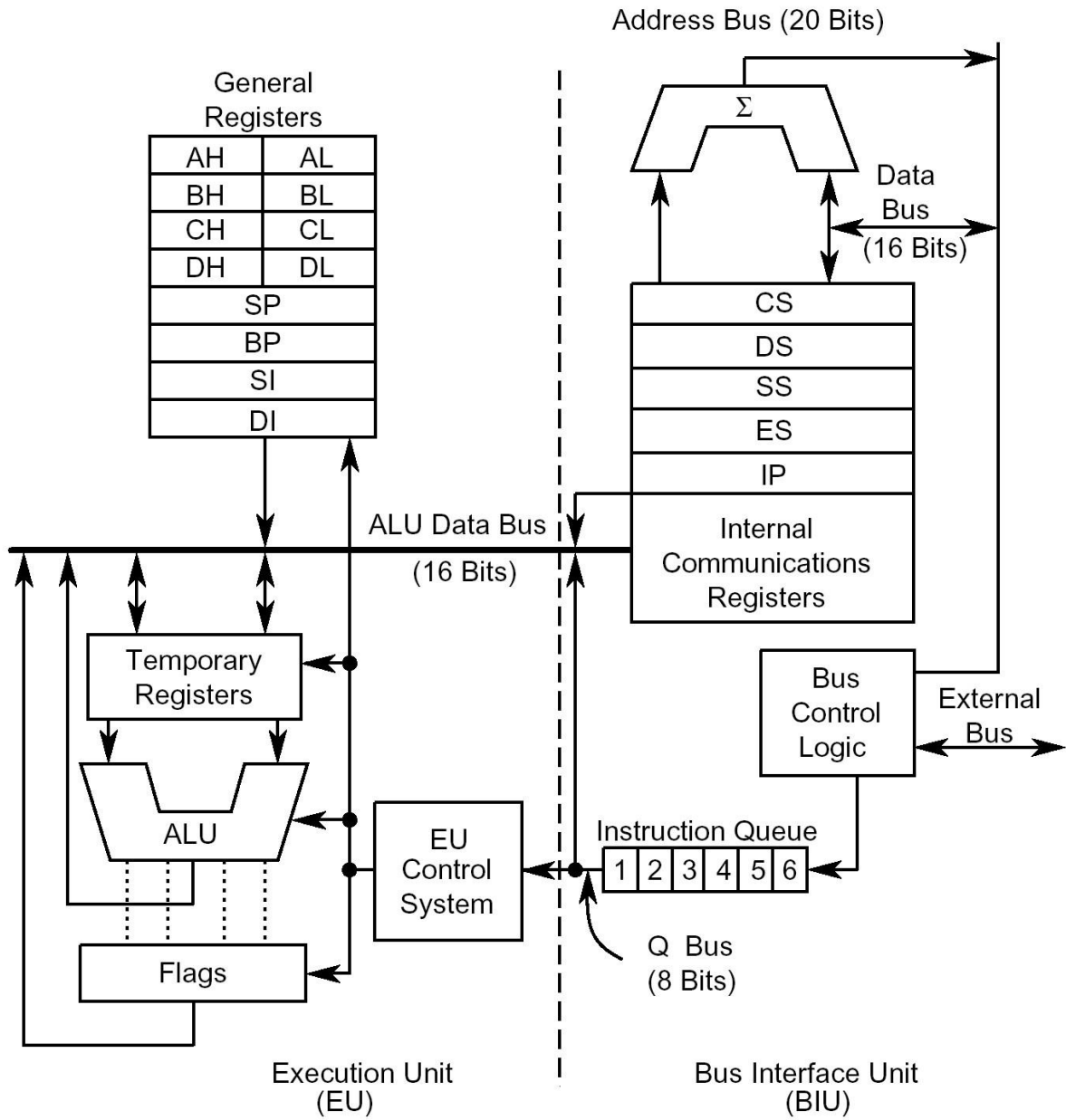


3. 80x86 Mikroişlemci Donanımı

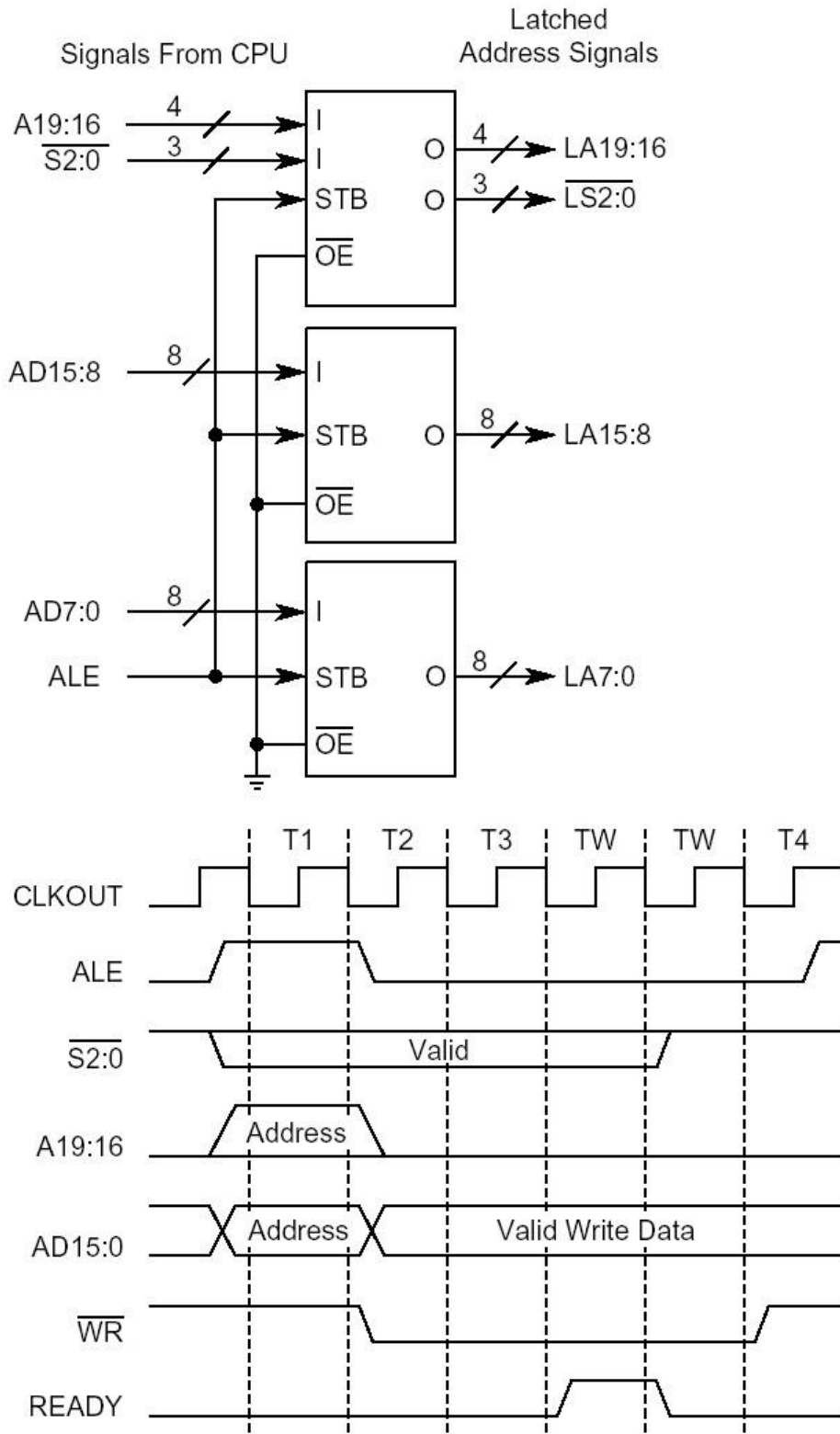
3.1. 8086 Uç ayrıntıları



3.1.1. 8086 İç Blok Diyagramı

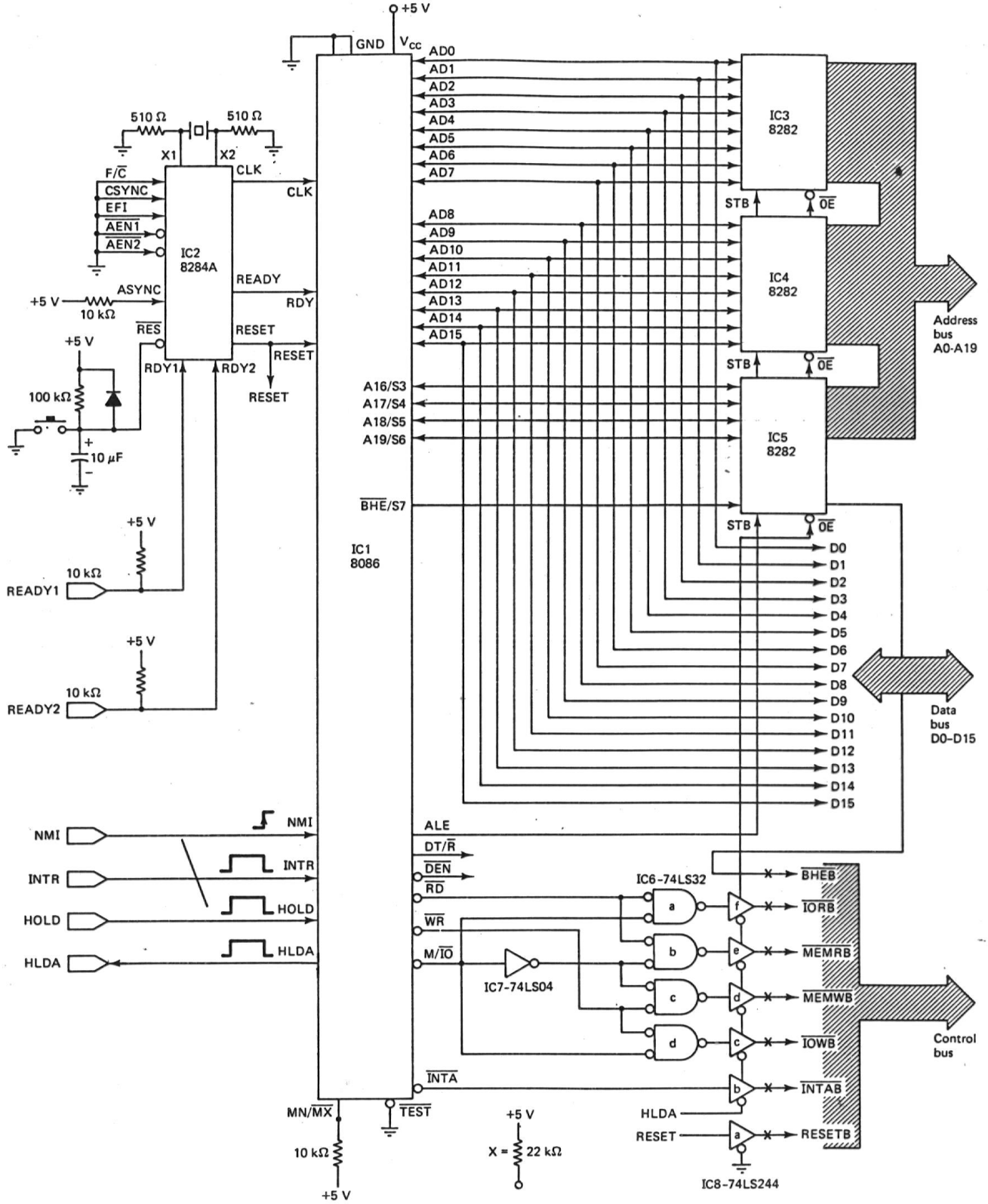


3.1.2. 8086 Adres/Veri Yolunun Ayrılması

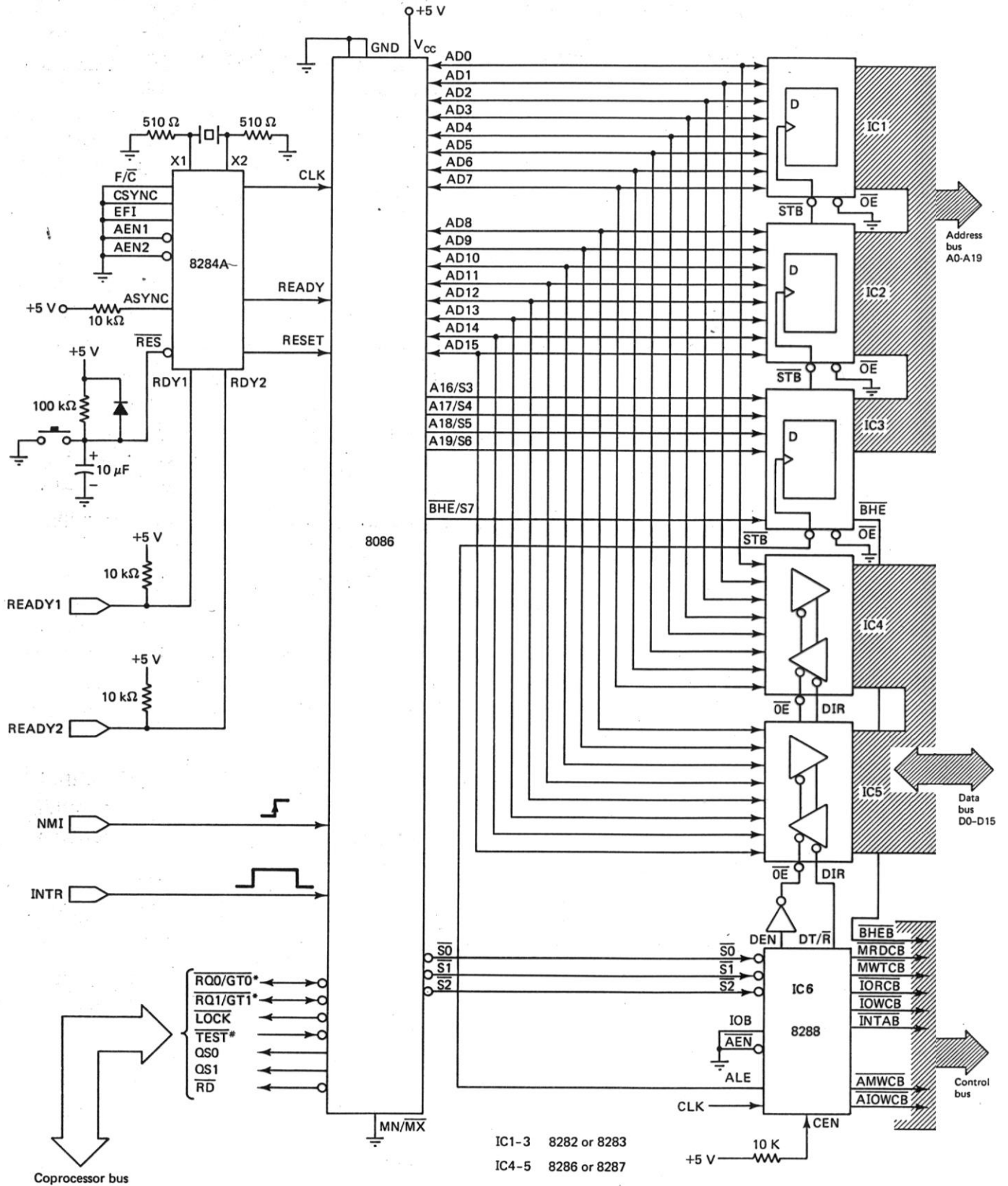


3.2. 8086 Mikroişlemci Çalışma Şekilleri

3.2.1. 8086 Mikroişlemcisinin Minimum Çalışma Şekli



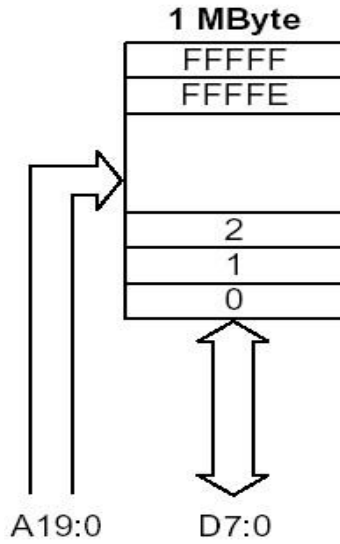
3.2.2. 8086 Mikroişlemcisinin Maksimum Çalışma Şekli



3.3. 8086 Fiziksel Bellek Yapısı

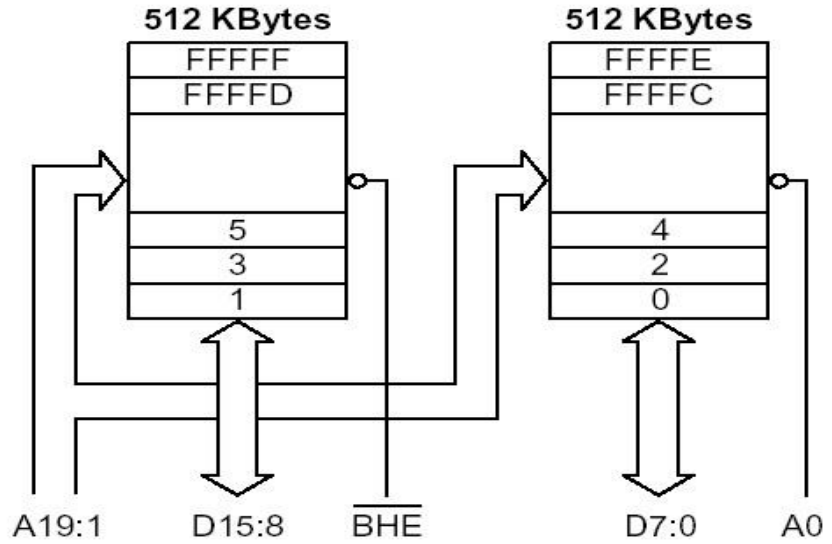
8-Bit Sistemler için Bellek Haritasının Fiziksel gerçektelemesi

Physical Implementation
of the Address Space for
8-Bit Systems



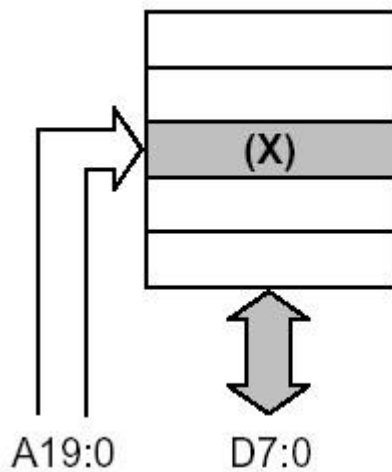
16-Bit Sistemler için Bellek Haritasının Fiziksel gerçektelemesi

Physical Implementation
of the Address Space for
16-Bit Systems

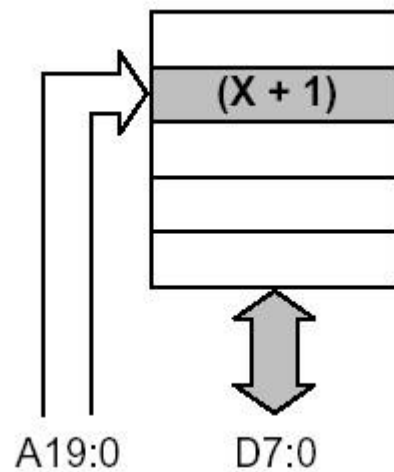


8-Bit Sistemler (8-Bit Veri Yolu) için Kelime transferi

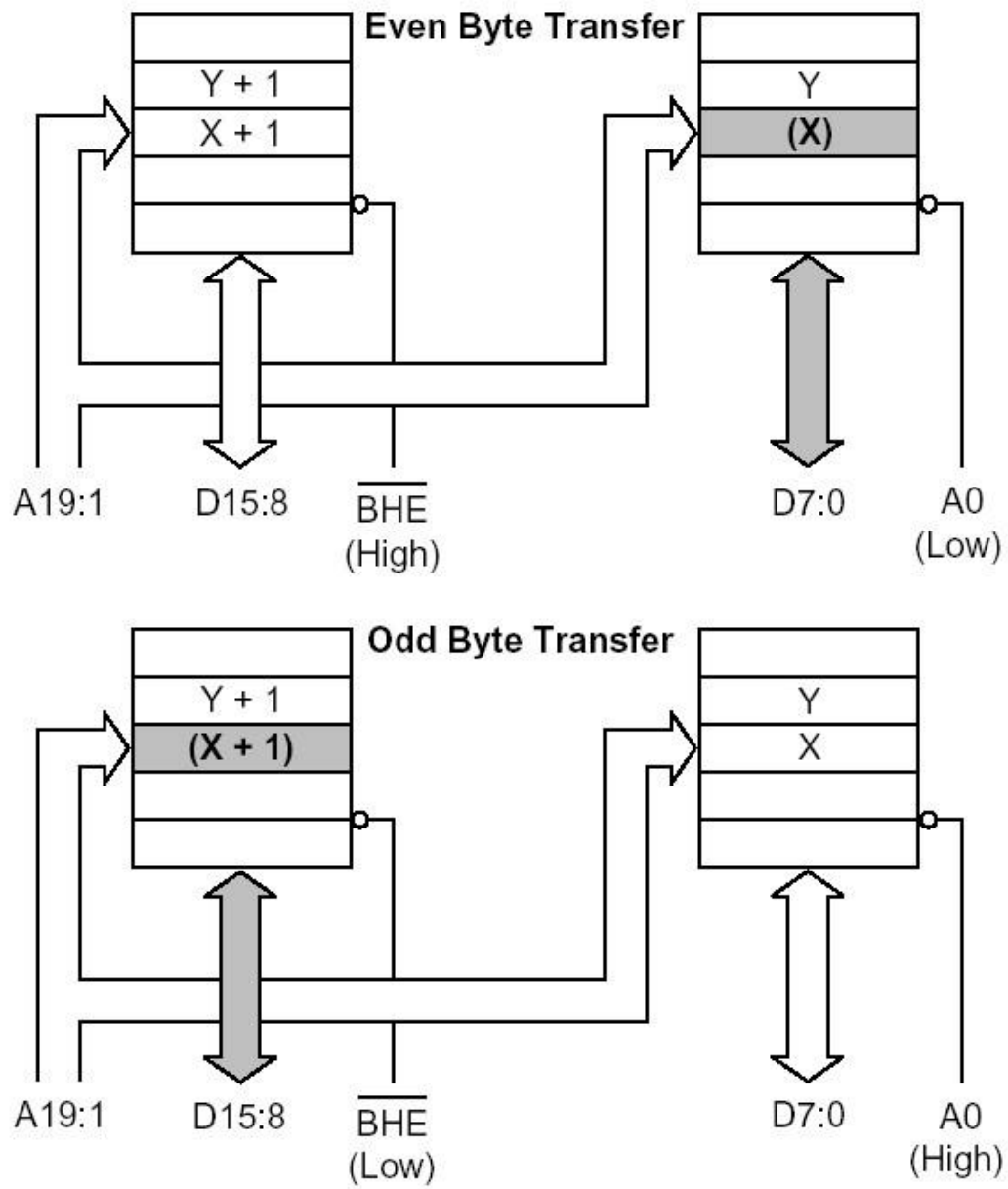
First Bus Cycle



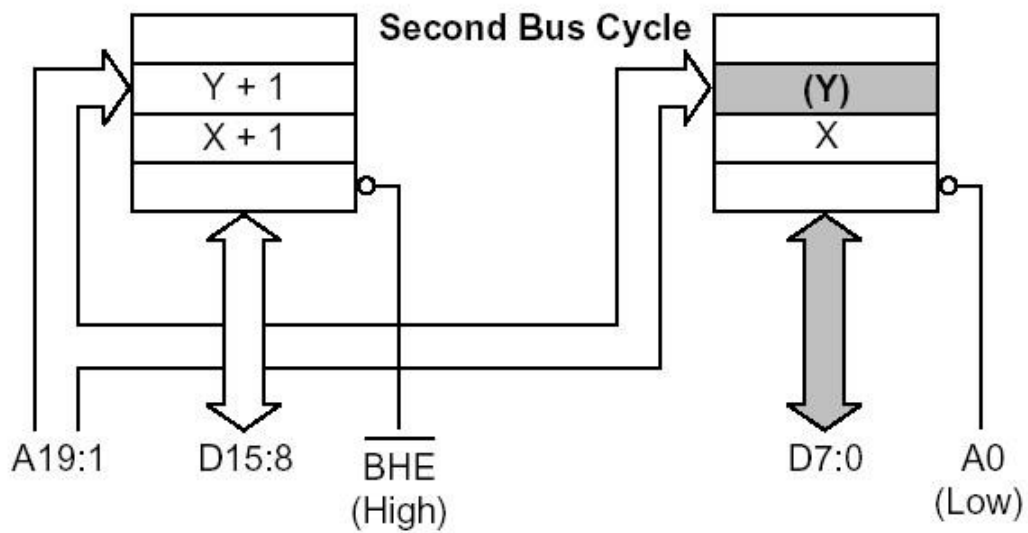
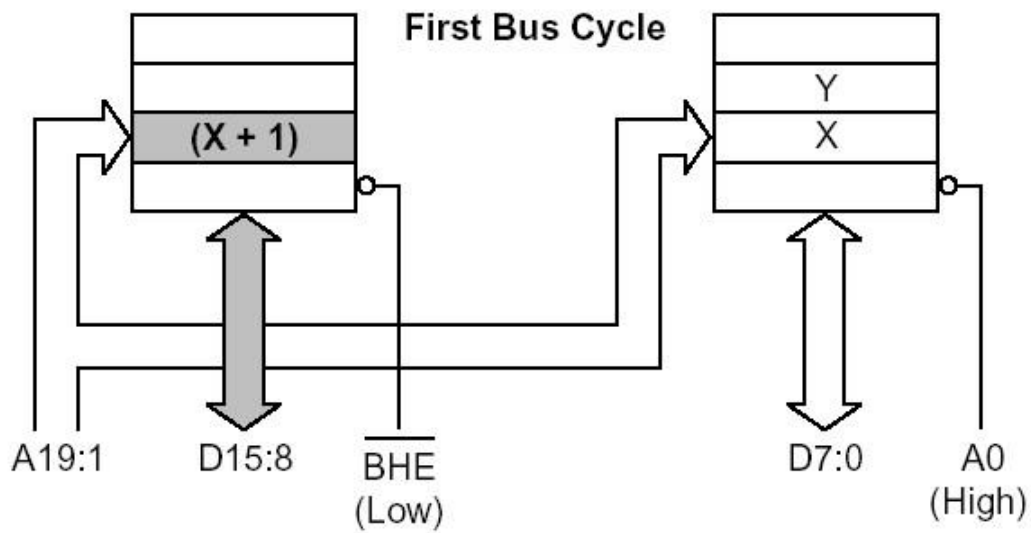
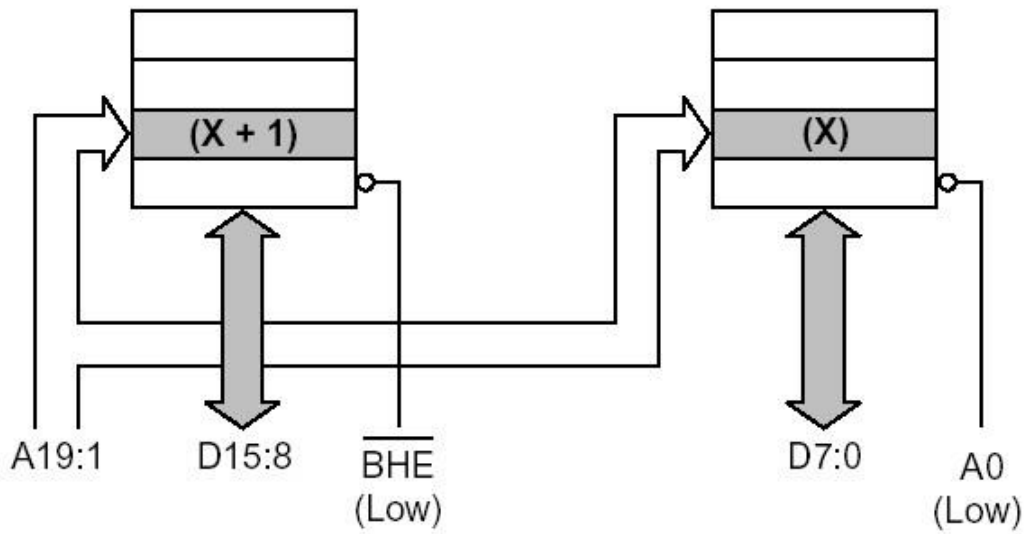
Second Bus Cycle



16-Bit Sistemler (16-Bit Veri Yolu) için Bayt Transferleri

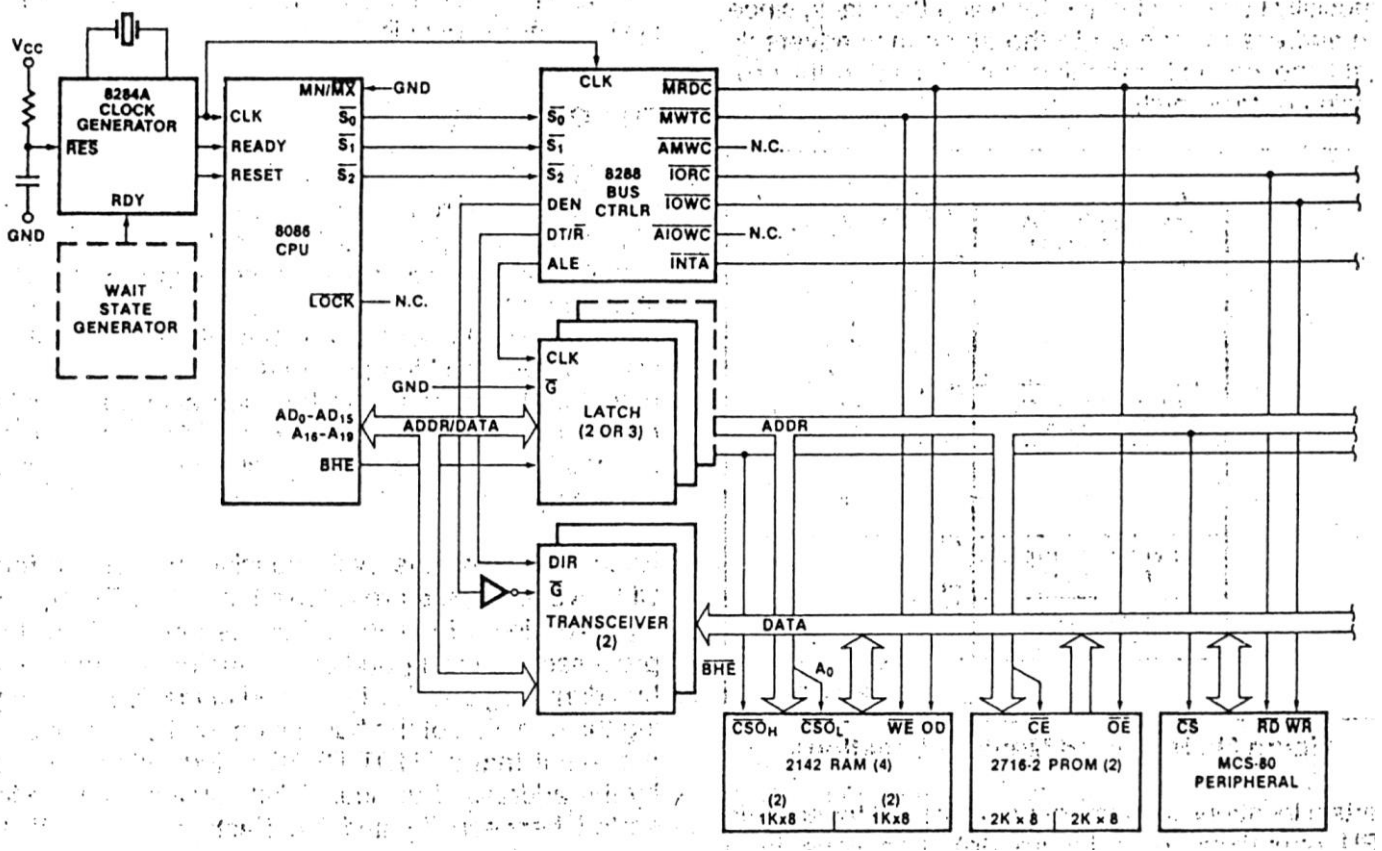


16-Bit Sistemler (16-Bit Veri Yolu) için Kelime Transferleri

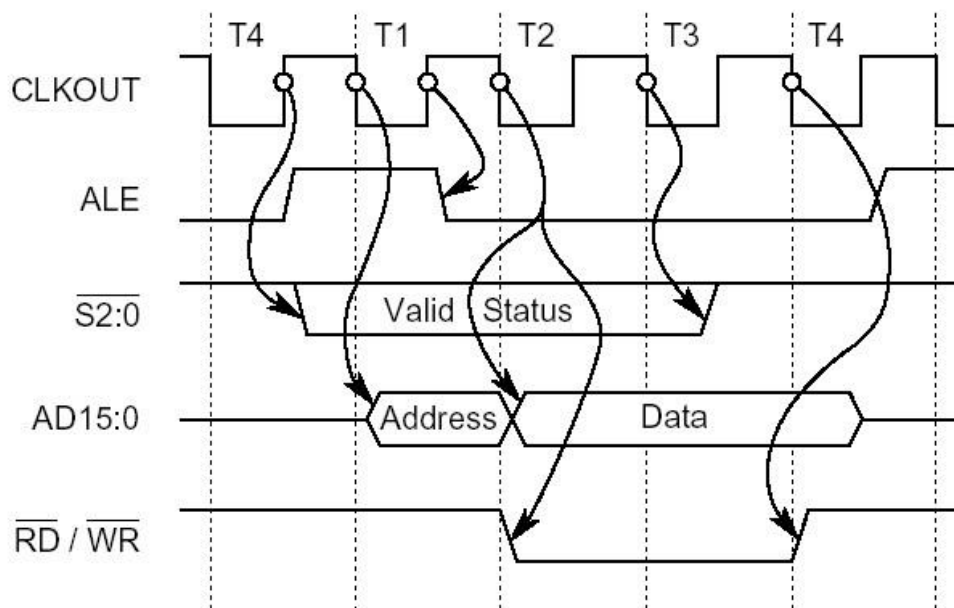
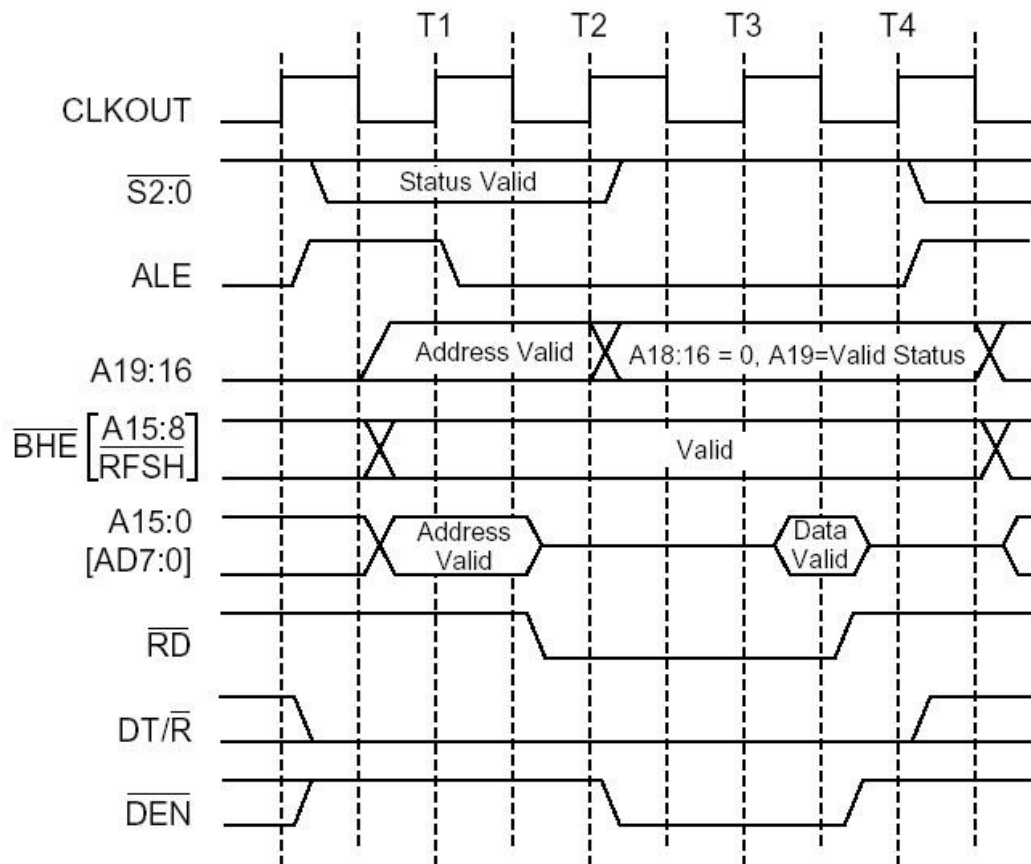


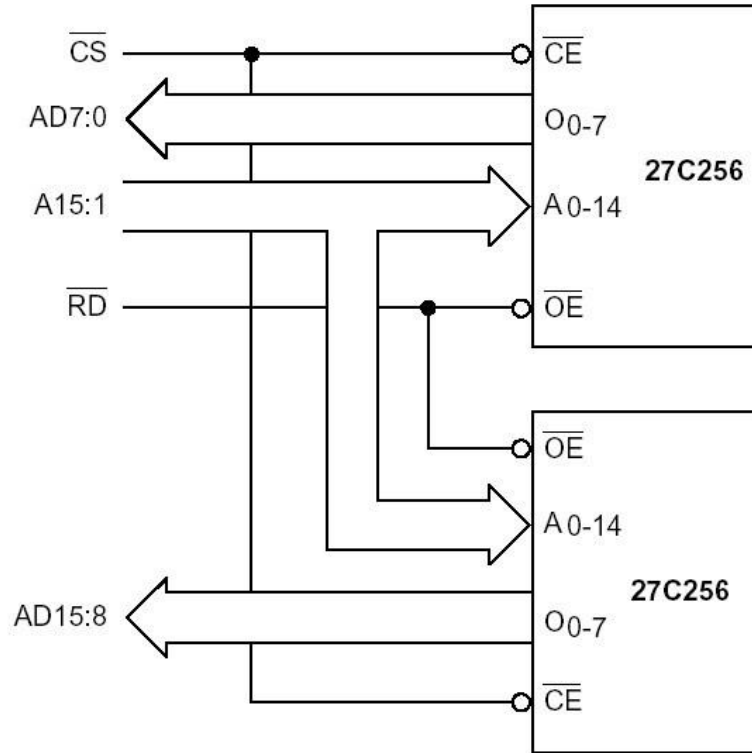


3.4.2. 8086 Mikroişlemci Temelli Maksimum Sistem Yapısı

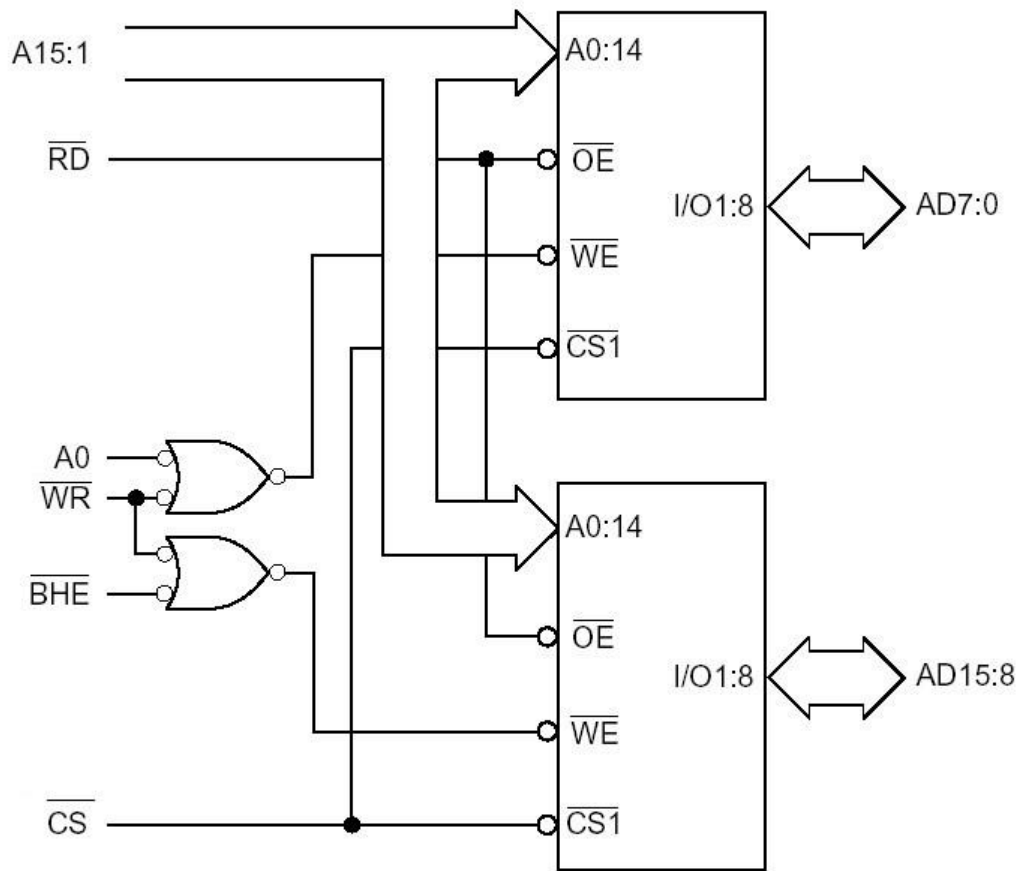


3.5. Ana Bellek Birimi



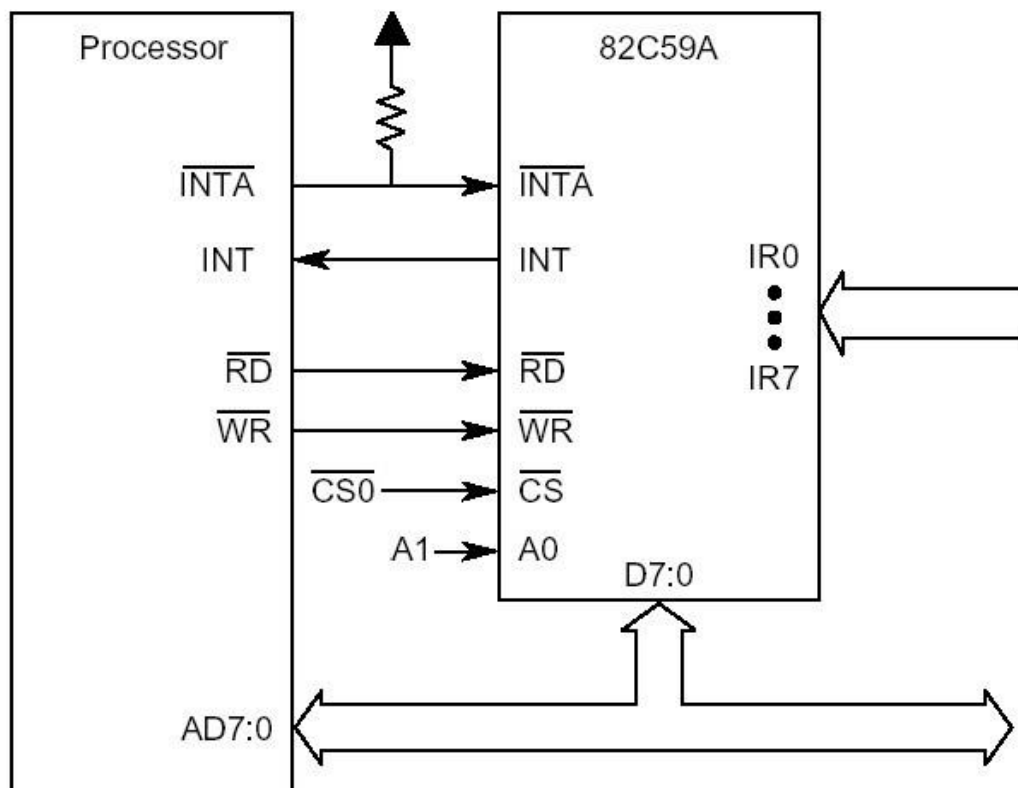
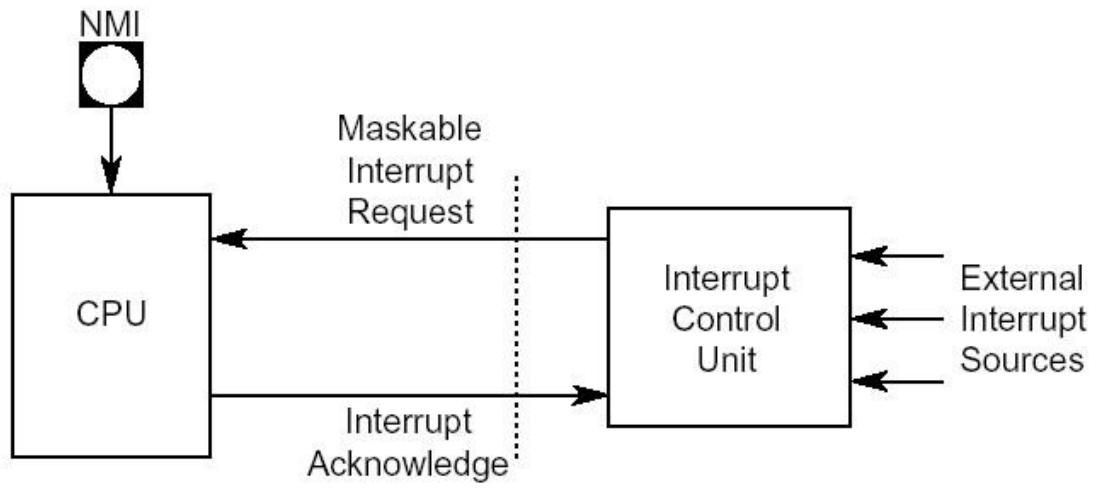


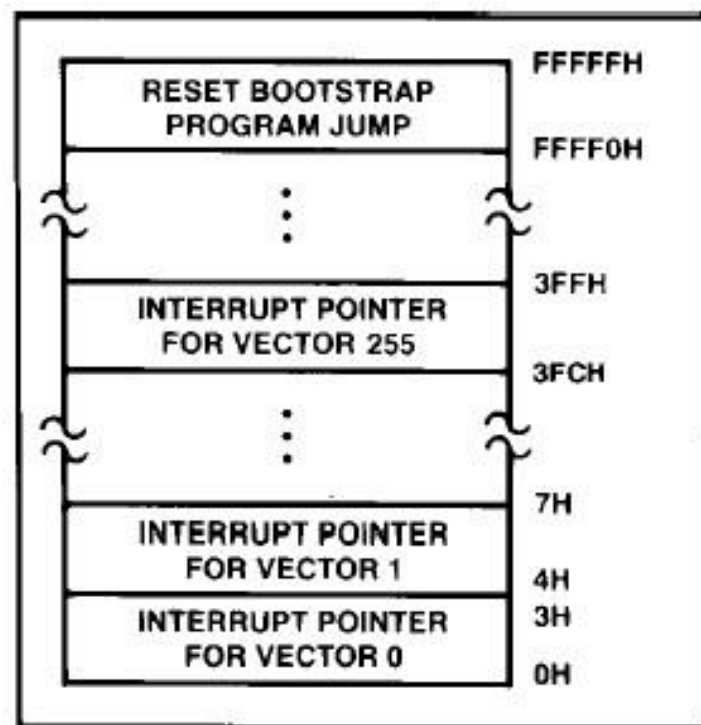
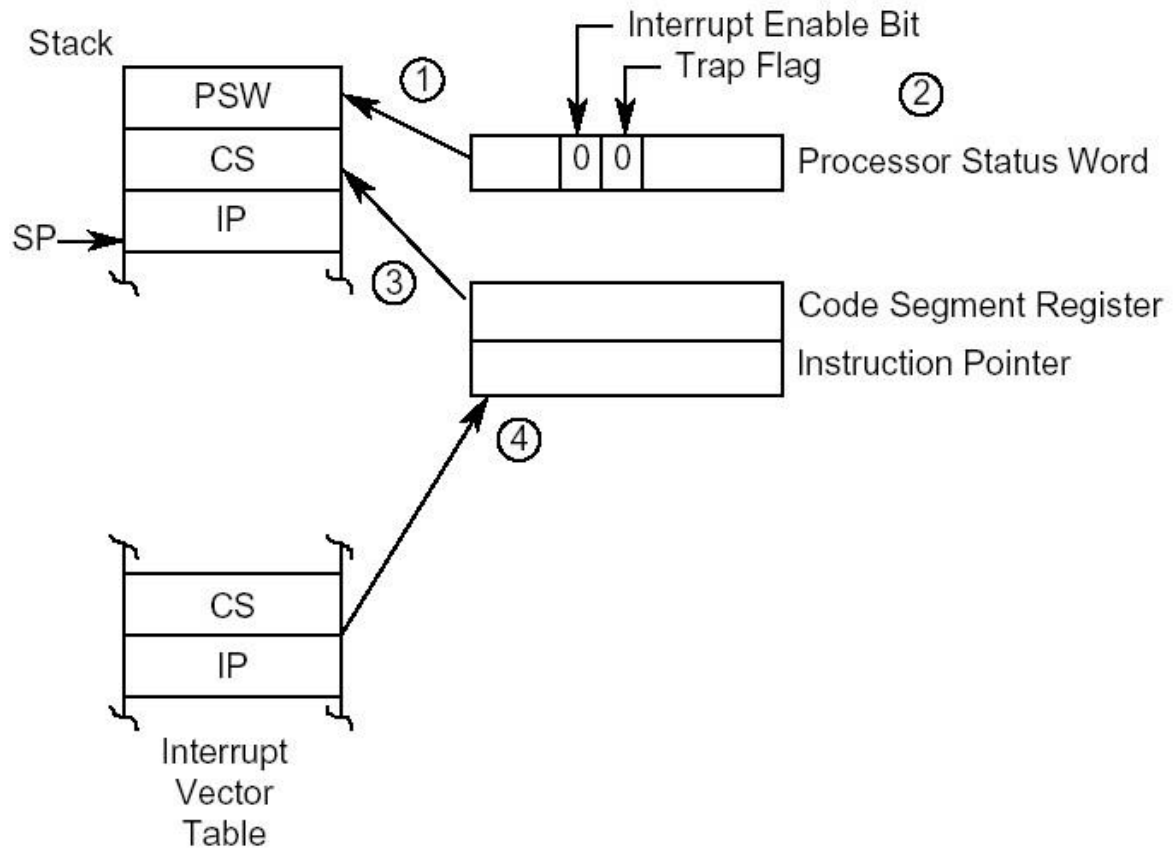
Note: A_0 and $\overline{BHE}$ are not used.



RAM Bağlantısı

3.6. 8086 Kesme Yapısı

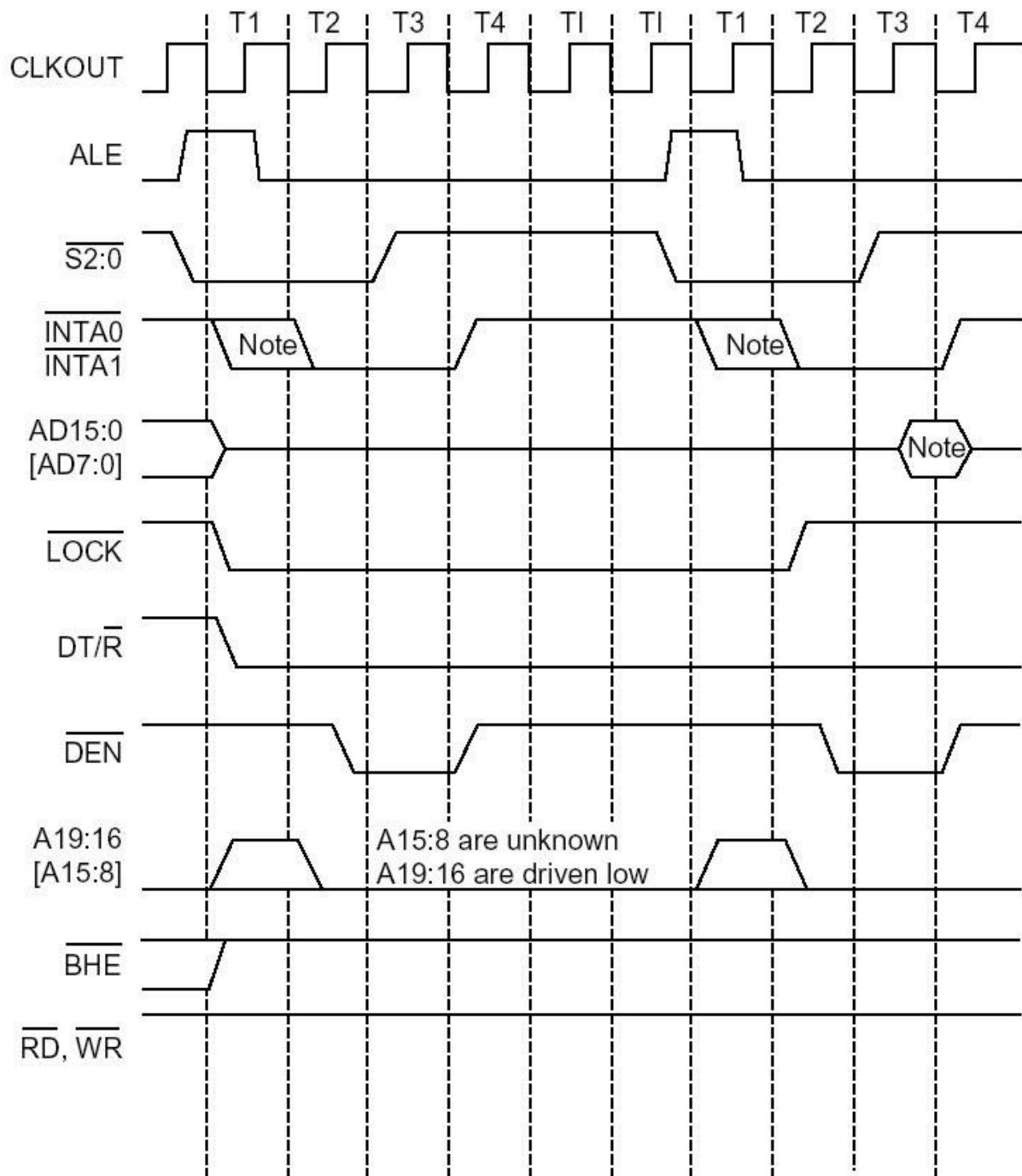




INITIAL CS:IP VALUE IS F000:FFF0.

Memory Address	Table Entry	Vector Definition	Memory Address	Table Entry	Vector Definition
3FE	CS	Type 255	2E	CS	Type 11 - DMA1
3FC	IP		2C	IP	
		User Available	2A	CS	Type 10 - DMA0
			28	IP	
82	CS	Type 32	26	CS	Type 9 - Reserved
80	IP		24	IP	
7E	CS	Type 31	22	CS	Type 8 - Timer 0
7C	IP		20	IP	
		Reserved	1E	CS	Type 7 - ESC Opcode
			1C	IP	
52	CS	Type 20	1A	CS	Type 6 - Unused Opcode
50	IP		18	IP	
4E	CS	Type 19 - Timer 2	16	CS	Type 5 - Array Bounds
4C	IP		14	IP	
4A	CS	Type 18 - Timer 1	12	CS	Type 4 - Overflow
48	IP		10	IP	
46	CS	Type 17 - Reserved	0E	CS	Type 3 - Breakpoint
44	IP		0C	IP	
42	CS	Type 16 - Numerics	0A	CS	Type 2 - NMI
40	IP		08	IP	
3E	CS	Type 15 - INT3	06	CS	Type 1 - Single-Step
3C	IP		04	IP	
3A	CS	Type 14 - INT2	02	CS	Type 0 - Divide Error
38	IP		00	IP	
36	CS	Type 13 - INT1			
34	IP				
32	CS	Type 12 - INT0			
30	IP				

CS = Code Segment Value
 IP = Instruction Pointer Value



NOTE: Vector Type is read from AD7:0 only.
INTA occurs during T2 in slave mode.